

Micropower Voltage Reference

ISL21010

The [ISL21010](#) is a precision, low dropout micropower bandgap voltage reference in a space-saving SOT-23 package. It operates from a single 2.2V to 5.5V supply (minimum voltage is dependent on voltage option) and provides a $\pm 0.2\%$ accurate reference. The ISL21010 provides up to 25mA output current sourcing with low 150mV dropout voltage.

Output voltage options include 1.024V, 1.2V, 1.5V, 2.048V, 2.5V, 3.0V, 3.3V and 4.096V. The low supply current and low dropout voltage combined with high accuracy make the ISL21010 ideal for precision battery powered applications.

Applications

- Battery management/monitoring
- Low power standby voltages
- Portable instrumentation
- Consumer/medical electronics
- Lower cost industrial and instrumentation
- Power regulation circuits
- Control loops and compensation networks
- LED/diode supply

Features

- Reference output voltages 1.024V, 1.25V, 1.5V, 2.048V, 2.5V, 3.0V, 3.3V, 4.096V
- Precision 0.2% initial accuracy
- Input voltage range:
 - ISL21010-10, -12, -15 -20 2.2V to 5.5V
 - ISL21010-25 2.6V to 5.5V
 - ISL21010-30 3.1V to 5.5V
 - ISL21010-33 3.4V to 5.5V
 - ISL21010-41 4.2V to 5.5V
- Output current source capability 25mA
- Operating temperature range. -40°C to $+125^{\circ}\text{C}$
- Output voltage noise ($V_{\text{OUT}} = 2.048\text{V}$) $58\mu\text{V}_{\text{P-P}}$ (0.1Hz to 10Hz)
- Supply current $48\mu\text{A}$ (typ)
- Tempco 50ppm/ $^{\circ}\text{C}$
- Package 3 Ld SOT-23
- Pb-free (RoHS compliant)

Related Literature

- [AN1819](#), "ISL21010XXEV1Z User Guide"
- [AN1883](#), "Low-Side Low Cost Current Sense Amplifier"

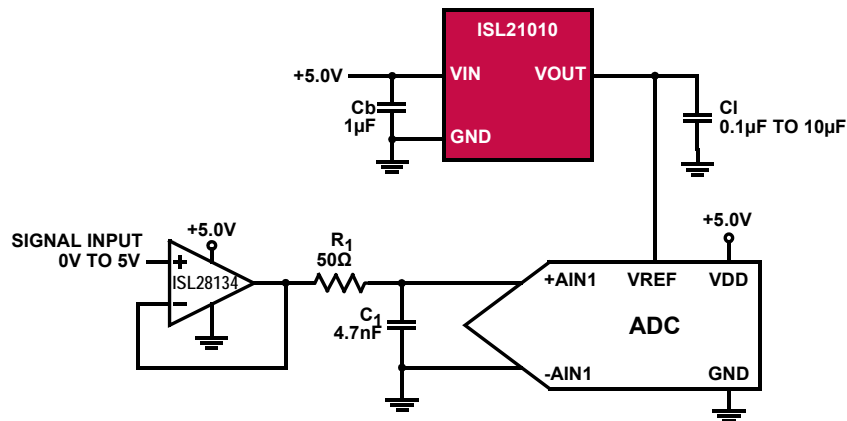


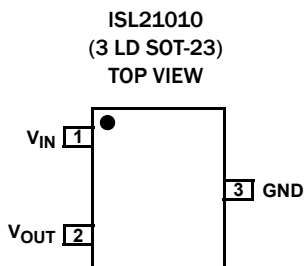
FIGURE 1. TYPICAL APPLICATION DIAGRAM

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Pin Configuration



Pin Descriptions

PIN NUMBER	PIN NAME	DESCRIPTION
1	V _{IN}	Input Voltage Connection
2	V _{OUT}	Voltage Reference Output
3	GND	Ground Connection

Ordering Information

PART NUMBER (Notes 1, 2, 3, 4)	PART MARKING	TAPE & REEL QUANTITY (UNITS)	V _{OUT} OPTION (V)	INITIAL ACCURACY (%)	TEMP. RANGE (°C)	PACKAGE (RoHS Compliant)	PKG. DWG. #
ISL21010DFH310Z-TK	BEBA	1k	1.024	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010DFH310Z-T7A	BEBA	250	1.024	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010DFH312Z-TK	BECA	1k	1.25	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010DFH312Z-T7A	BECA	250	1.25	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH315Z-TK	BDRA	1k	1.5	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH315Z-T7A	BDRA	250	1.5	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH320Z-TK	BDSA	1k	2.048	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH320Z-T7A	BDSA	250	2.048	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH325Z-TK	BDTA	1k	2.5	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH325Z-T7A	BDTA	250	2.5	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH330Z-TK	BDVA	1k	3.0	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH330Z-T7A	BDVA	250	3.0	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH333Z-TK	BDWA	1k	3.3	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH333Z-T7A	BDWA	250	3.3	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH341Z-TK	BDYA	1k	4.096	±0.2	-40 to +125	3 Ld SOT-23	P3.064
ISL21010CFH341Z-T7A	BDYA	250	4.096	±0.2	-40 to +125	3 Ld SOT-23	P3.064

NOTES:

1. Please refer to [TB347](#) for details on reel specifications.
2. These Intersil Pb-free plastic packaged products employ special Pb-free material sets, molding compounds/die attach materials, and 100% matte tin plate plus anneal (e3 termination finish, which is RoHS compliant and compatible with both SnPb and Pb-free soldering operations). Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.
3. For Moisture Sensitivity Level (MSL), please see device information page for [ISL21010](#). For more information on MSL please see Tech Brief [TB363](#).
4. The part marking is located on the bottom of the part.

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Absolute Maximum Ratings

Max Voltage	
V_{IN} to GND	-0.5V to +6.5V
V_{OUT} (pin) to GND (10s)	-0.5V to V_{IN} +0.5V
Input Voltage Slew Rate (Max)	1V/ μ s
Temperature Range (Industrial)	-40°C to +125°C
ESD Rating	
Human Body Model	5.5kV
Machine Model	300V
Charged Device Model	2kV

Thermal Information

Thermal Resistance (Typical)	θ_{JA} (°C/W)	θ_{JC} (°C/W)
3 Ld SOT-23 Package (Notes 5, 6)	275	110
Continuous Power Dissipation ($T_A = +125^\circ\text{C}$)	99mW	
Storage Temperature Range	-65°C to +150°C	
Pb-Free Reflow Profile	see TB493	

Recommended Operating Conditions

Temperature	-40°C to +125°C
Supply Voltage	
$V_{OUT} = 1.024\text{V}, 1.25\text{V}, 1.5\text{V}, 2.048\text{V}$	2.2V to 5.5V
$V_{OUT} = 2.5\text{V}$	2.6V to 5.5V
$V_{OUT} = 3.0\text{V}$	3.1V to 5.5V
$V_{OUT} = 3.3\text{V}$	3.4V to 5.5V
$V_{OUT} = 4.096\text{V}$	4.2V to 5.5V

CAUTION: Do not operate at or near the maximum ratings listed for extended periods of time. Exposure to such conditions may adversely impact product reliability and result in failures not covered by warranty.

NOTES:

- θ_{JA} is measured with the component mounted on a high effective thermal conductivity test board in free air. See Tech Brief [TB379](#) for details.
- For θ_{JC} , the “case temp” location is taken at the package top center.
- Post-reflow drift for the ISL21010 devices may shift up to 4.0mV based on simulated reflow at 260°C peak temperature, three passes. The system design engineer must take this into account when considering the reference voltage after assembly.

Electrical Specifications (ISL21010-10, $V_{OUT} = 1.024\text{V}$) $V_{IN} = 3.0\text{V}$, $T_A = +25^\circ\text{C}$, $I_{OUT} = 0\text{A}$, unless otherwise specified. **Boldface limits apply across the operating temperature range, -40°C to +125°C.**

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNIT
V_{OUT}	Output Voltage			1.024		V
V_{OA}	V_{OUT} Accuracy at $T_A = +25^\circ\text{C}$ (Note 7)		-0.2		+0.2	%
TC V_{OUT}	Output Voltage Temperature Coefficient (Note 9)			15	50	ppm/ $^\circ\text{C}$
V_{IN}	Input Voltage Range		2.2		5.5	V
I_{IN}	Supply Current	$T_A = +25^\circ\text{C}$		46	80	μA
		$T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$		60	100	μA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	$2.2\text{V} \leq V_{IN} \leq 5.5\text{V}$		5	100	$\mu\text{V}/\text{V}$
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	Sourcing: $0\text{mA} \leq I_{OUT} \leq 25\text{mA}$		15	110	$\mu\text{V}/\text{mA}$
		Sinking: $-1\text{mA} \leq I_{OUT} \leq 0\text{mA}$		17		$\mu\text{V}/\text{mA}$
I_{SC}	Short-Circuit Current	$T_A = +25^\circ\text{C}$, V_{OUT} tied to GND		118		mA
t_R	Turn-On Settling Time	$V_{OUT} = \pm 0.1\%$, $C_{OUT} = 1\mu\text{F}$		300		μs
	Ripple Rejection	$f = 120\text{Hz}$		70		dB
e_N	Output Voltage Noise	$0.1\text{Hz} \leq f \leq 10\text{Hz}$		24		μV_{P-P}
V_N	Broadband Voltage Noise	$10\text{Hz} \leq f \leq 1\text{kHz}$		14		μV_{RMS}
$\Delta V_{OUT}/\Delta T_A$	Thermal Hysteresis (Note 11)	$\Delta T_A = +165^\circ\text{C}$		100		ppm
$\Delta V_{OUT}/\Delta t$	Long Term Stability	1000 hours, $T_A = +25^\circ\text{C}$		110		ppm

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Electrical Specifications (ISL21010-12, $V_{OUT} = 1.25V$) $V_{IN} = 3.0V$, $T_A = +25^\circ C$, $I_{OUT} = 0A$, unless otherwise specified.
Boldface limits apply across the operating temperature range, $-40^\circ C$ to $+125^\circ C$.

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNIT
V_{OUT}	Output Voltage			1.25		V
V_{OA}	V_{OUT} Accuracy at $T_A = +25^\circ C$ (Note 7)		-0.2		+0.2	%
TC V_{OUT}	Output Voltage Temperature Coefficient (Note 9)			15	50	ppm/ $^\circ C$
V_{IN}	Input Voltage Range		2.2		5.5	V
I_{IN}	Supply Current	$T_A = +25^\circ C$		46	80	μA
		$T_A = -40^\circ C$ to $+125^\circ C$			100	μA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	$2.2V \leq V_{IN} \leq 5.5V$		1	100	$\mu V/V$
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	Sourcing: $0mA \leq I_{OUT} \leq 25mA$		35	110	$\mu V/mA$
		Sinking: $-1mA \leq I_{OUT} \leq 0mA$		50		$\mu V/mA$
I_{SC}	Short-Circuit Current	$T_A = +25^\circ C$, V_{OUT} tied to GND		118		mA
t_R	Turn-On Settling Time	$V_{OUT} = \pm 0.1\%$, $C_{OUT} = 1\mu F$		300		μs
	Ripple Rejection	$f = 120Hz$		68		dB
e_N	Output Voltage Noise	$0.1Hz \leq f \leq 10Hz$		27		μV_{P-P}
V_N	Broadband Voltage Noise	$10Hz \leq f \leq 1kHz$		17		μV_{RMS}
$\Delta V_{OUT}/\Delta T_A$	Thermal Hysteresis (Note 11)	$\Delta T_A = +165^\circ C$		100		ppm
$\Delta V_{OUT}/\Delta t$	Long Term Stability	1000 hours, $T_A = +25^\circ C$		110		ppm

Electrical Specifications (ISL21010-15, $V_{OUT} = 1.5V$) $V_{IN} = 3.0V$, $T_A = +25^\circ C$, $I_{OUT} = 0A$, unless otherwise specified.
Boldface limits apply across the operating temperature range, $-40^\circ C$ to $+125^\circ C$.

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNIT
V_{OUT}	Output Voltage			1.5		V
V_{OA}	V_{OUT} Accuracy at $T_A = +25^\circ C$ (Note 7)		-0.2		+0.2	%
TC V_{OUT}	Output Voltage Temperature Coefficient (Note 9)			15	50	ppm/ $^\circ C$
V_{IN}	Input Voltage Range		2.2		5.5	V
I_{IN}	Supply Current	$T_A = +25^\circ C$		46	80	μA
		$T_A = -40^\circ C$ to $+125^\circ C$			100	μA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	$2.2V \leq V_{IN} \leq 5.5V$		9	100	$\mu V/V$
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	Sourcing: $0mA \leq I_{OUT} \leq 25mA$		37	110	$\mu V/mA$
		Sinking: $-1mA \leq I_{OUT} \leq 0mA$		50		$\mu V/mA$
I_{SC}	Short-Circuit Current	$T_A = +25^\circ C$, V_{OUT} tied to GND		118		mA
t_R	Turn-On Settling Time	$V_{OUT} = \pm 0.1\%$, $C_{OUT} = 1\mu F$		300		μs
	Ripple Rejection	$f = 120Hz$		66		dB
e_N	Output Voltage Noise	$0.1Hz \leq f \leq 10Hz$		35		μV_{P-P}
V_N	Broadband Voltage Noise	$10Hz \leq f \leq 1kHz$		20		μV_{RMS}
$\Delta V_{OUT}/\Delta T_A$	Thermal Hysteresis (Note 11)	$\Delta T_A = +165^\circ C$		100		ppm
$\Delta V_{OUT}/\Delta t$	Long Term Stability	1000 hours, $T_A = +25^\circ C$		110		ppm

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Electrical Specifications (ISL21010-20, V_{OUT} = 2.048V) V_{IN} = 3.0V, T_A = +25°C, I_{OUT} = 0A, unless otherwise specified. **Boldface limits apply across the operating temperature range, -40°C to +125°C.**

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNIT
V _{OUT}	Output Voltage			2.048		V
V _{OA}	V _{OUT} Accuracy at T _A = +25°C (Note 7)		-0.2		+0.2	%
TC V _{OUT}	Output Voltage Temperature Coefficient (Note 9)			15	50	ppm/°C
V _{IN}	Input Voltage Range		2.2		5.5	V
I _{IN}	Supply Current	T _A = +25°C		46	80	μA
		T _A = -40°C to +125°C			100	μA
ΔV _{OUT} /ΔV _{IN}	Line Regulation	2.2 V ≤ V _{IN} ≤ 5.5V		37	130	μV/V
ΔV _{OUT} /ΔI _{OUT}	Load Regulation	Sourcing: 0mA ≤ I _{OUT} ≤ 25mA		18	110	μV/mA
		Sinking: -1mA ≤ I _{OUT} ≤ 0mA		10		μV/mA
I _{SC}	Short-Circuit Current	T _A = +25°C, V _{OUT} tied to GND		118		mA
t _R	Turn-On Settling Time	V _{OUT} = ±0.1%, C _{OUT} = 1μF		300		μs
	Ripple Rejection	f = 120Hz		66		dB
e _N	Output Voltage Noise	0.1Hz ≤ f ≤ 10Hz		58		μV _{P-P}
V _N	Broadband Voltage Noise	10Hz ≤ f ≤ 1kHz		26		μV _{RMS}
ΔV _{OUT} /ΔT _A	Thermal Hysteresis (Note 11)	ΔT _A = +165°C		100		ppm
ΔV _{OUT} /Δt	Long Term Stability	1000 hours, T _A = +25°C		50		ppm

Electrical Specifications (ISL21010-25, V_{OUT} = 2.5V) V_{IN} = 3.0V, T_A = +25°C, I_{OUT} = 0A, unless otherwise specified. **Boldface limits apply across the operating temperature range, -40°C to +125°C.**

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNIT
V _{OUT}	Output Voltage			2.5		V
V _{OA}	V _{OUT} Accuracy at T _A = +25°C (Note 7)		-0.2		+0.2	%
TC V _{OUT}	Output Voltage Temperature Coefficient (Note 9)			15	50	ppm/°C
V _{IN}	Input Voltage Range		2.6		5.5	V
I _{IN}	Supply Current	T _A = +25°C		46	80	μA
		T _A = -40°C to +125°C			100	μA
ΔV _{OUT} /ΔV _{IN}	Line Regulation	2.6 V ≤ V _{IN} ≤ 5.5V		62	245	μV/V
ΔV _{OUT} /ΔI _{OUT}	Load Regulation	Sourcing: 0mA ≤ I _{OUT} ≤ 25mA		29	110	μV/mA
		Sinking: -1mA ≤ I _{OUT} ≤ 0mA		50		μV/mA
V _{INDO}	Dropout Voltage (Note 10)	I _{OUT} = 10mA		60	150	mV
I _{SC}	Short-Circuit Current	T _A = +25°C, V _{OUT} tied to GND		118		mA
t _R	Turn-On Settling Time	V _{OUT} = ±0.1%, C _{OUT} = 1μF		300		μs
	Ripple Rejection	f = 120Hz		62		dB
e _N	Output Voltage Noise	0.1Hz ≤ f ≤ 10Hz		67		μV _{P-P}
V _N	Broadband Voltage Noise	10Hz ≤ f ≤ 1kHz		37		μV _{RMS}
ΔV _{OUT} /ΔT _A	Thermal Hysteresis (Note 11)	ΔT _A = +165°C		100		ppm
ΔV _{OUT} /Δt	Long Term Stability	1000 hours, T _A = +25°C		110		ppm

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Electrical Specifications (ISL21010-30, $V_{OUT} = 3.0V$) $V_{IN} = 5.0V$, $T_A = +25^\circ C$, $I_{OUT} = 0A$, unless otherwise specified.
Boldface limits apply across the operating temperature range, $-40^\circ C$ to $+125^\circ C$.

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNIT
V_{OUT}	Output Voltage			3.0		V
V_{OA}	V_{OUT} Accuracy at $T_A = +25^\circ C$ (Note 7)		-0.2		+0.2	%
TC V_{OUT}	Output Voltage Temperature Coefficient (Note 9)			15	50	ppm/ $^\circ C$
V_{IN}	Input Voltage Range		3.1		5.5	V
I_{IN}	Supply Current	$T_A = +25^\circ C$		48	80	μA
		$T_A = -40^\circ C$ to $+125^\circ C$			100	μA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	$3.1V \leq V_{IN} \leq 5.5V$		73	230	$\mu V/V$
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	Sourcing: $0mA \leq I_{OUT} \leq 25mA$		48	110	$\mu V/mA$
		Sinking: $-1mA \leq I_{OUT} \leq 0mA$		10		$\mu V/mA$
V_{INDO}	Dropout Voltage (Note 10)	$I_{OUT} = 10mA$		60	150	mV
I_{SC}	Short-Circuit Current	$T_A = +25^\circ C$, V_{OUT} tied to GND		126		mA
t_R	Turn-On Settling Time	$V_{OUT} = \pm 0.1\%$, $C_{OUT} = 1\mu F$		300		μs
	Ripple Rejection	$f = 120Hz$		62		dB
e_N	Output Voltage Noise	$0.1Hz \leq f \leq 10Hz$		86		μV_{p-p}
V_N	Broadband Voltage Noise	$10Hz \leq f \leq 1kHz$		36		μV_{RMS}
$\Delta V_{OUT}/\Delta T_A$	Thermal Hysteresis (Note 11)	$\Delta T_A = +165^\circ C$		100		ppm
$\Delta V_{OUT}/\Delta t$	Long Term Stability	1000 hours, $T_A = +25^\circ C$		50		ppm

Electrical Specifications (ISL21010-33, $V_{OUT} = 3.3V$) $V_{IN} = 5.0V$, $T_A = +25^\circ C$, $I_{OUT} = 0A$, unless otherwise specified.
Boldface limits apply across the operating temperature range, $-40^\circ C$ to $+125^\circ C$.

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNIT
V_{OUT}	Output Voltage			3.3		V
V_{OA}	V_{OUT} Accuracy at $T_A = +25^\circ C$ (Note 7)		-0.2		+0.2	%
TC V_{OUT}	Output Voltage Temperature Coefficient (Note 9)			15	50	ppm/ $^\circ C$
V_{IN}	Input Voltage Range		3.4		5.5	V
I_{IN}	Supply Current	$T_A = +25^\circ C$		48	80	μA
		$T_A = -40^\circ C$ to $+125^\circ C$			100	μA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	$3.4V \leq V_{IN} \leq 5.5V$		80	320	$\mu V/V$
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	Sourcing: $0mA \leq I_{OUT} \leq 25mA$		45	110	$\mu V/mA$
		Sinking: $-1mA \leq I_{OUT} \leq 0mA$		10		$\mu V/mA$
V_{INDO}	Dropout Voltage (Note 10)	$I_{OUT} = 10mA$		60	150	mV
I_{SC}	Short-Circuit Current	$T_A = +25^\circ C$, V_{OUT} tied to GND		126		mA
t_R	Turn-On Settling Time	$V_{OUT} = \pm 0.1\%$, $C_{OUT} = 1\mu F$		300		μs
	Ripple Rejection	$f = 120Hz$		61		dB
e_N	Output Voltage Noise	$0.1Hz \leq f \leq 10Hz$		95		μV_{p-p}
V_N	Broadband Voltage Noise	$10Hz \leq f \leq 1kHz$		40		μV_{RMS}
$\Delta V_{OUT}/\Delta T_A$	Thermal Hysteresis (Note 11)	$\Delta T_A = +165^\circ C$		100		ppm
$\Delta V_{OUT}/\Delta t$	Long Term Stability	1000 hours, $T_A = +25^\circ C$		50		ppm

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Electrical Specifications (ISL21010-41, $V_{OUT} = 4.096V$) $V_{IN} = 5.0V$, $T_A = +25^\circ C$, $I_{OUT} = 0A$, unless otherwise specified. **Boldface limits apply across the operating temperature range, $-40^\circ C$ to $+125^\circ C$.**

PARAMETER	DESCRIPTION	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNIT
V_{OUT}	Output Voltage			4.096		V
V_{OA}	V_{OUT} Accuracy at $T_A = +25^\circ C$ (Note 7)		-0.2		+0.2	%
TC V_{OUT}	Output Voltage Temperature Coefficient (Note 9)			15	50	ppm/ $^\circ C$
V_{IN}	Input Voltage Range		4.2		5.5	V
I_{IN}	Supply Current	$T_A = +25^\circ C$		48	80	μA
		$T_A = -40^\circ C$ to $+125^\circ C$			100	μA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	$4.2 V \leq V_{IN} \leq 5.5 V$		106	550	$\mu V/V$
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	Sourcing: $0mA \leq I_{OUT} \leq 25mA$		50	140	$\mu V/mA$
		Sinking: $-1mA \leq I_{OUT} \leq 0mA$		50		$\mu V/mA$
V_{INDO}	Dropout Voltage (Note 10)	$I_{OUT} = 10mA$		60	150	mV
I_{SC}	Short-Circuit Current	$T_A = +25^\circ C$, V_{OUT} tied to GND		126		mA
t_R	Turn-On Settling Time	$V_{OUT} = \pm 0.1\%$, $C_{OUT} = 1\mu F$		300		μs
	Ripple Rejection	$f = 120Hz$		58		dB
e_N	Output Voltage Noise	$0.1Hz \leq f \leq 10Hz$		112		μV_{p-p}
V_N	Broadband Voltage Noise	$10Hz \leq f \leq 1kHz$		56		μV_{RMS}
$\Delta V_{OUT}/\Delta T_A$	Thermal Hysteresis (Note 11)	$\Delta T_A = +165^\circ C$		100		ppm
$\Delta V_{OUT}/\Delta t$	Long Term Stability	1000 hours, $T_A = +25^\circ C$		110		ppm

NOTES:

- Compliance to datasheet limits is assured by one or more methods: production test, characterization and/or design.
- Over the specified temperature range. Temperature coefficient is measured by the box method whereby the change in V_{OUT} is divided by the temperature range; in this case, $-40^\circ C$ to $+125^\circ C = +165^\circ C$.
- Dropout Voltage is the minimum $V_{IN} - V_{OUT}$ differential voltage measured at the point where V_{OUT} drops 1mV from $V_{IN} = \text{nominal}$ at $T_A = +25^\circ C$.
- Thermal Hysteresis is the change of V_{OUT} measured at $T_A = +25^\circ C$ after temperature cycling over a specified range, ΔT_A . V_{OUT} is read initially at $T_A = +25^\circ C$ for the device under test. The device is temperature cycled and a second V_{OUT} measurement is taken at $+25^\circ C$. The difference between the initial V_{OUT} reading and the second V_{OUT} reading is then expressed in ppm. For $\Delta T_A = +165^\circ C$, the device under test is cycled from $+25^\circ C$ to $-40^\circ C$ to $+125^\circ C$ to $+25^\circ C$.

Typical Performance Characteristics Curves ($V_{OUT} = 1.024V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified.

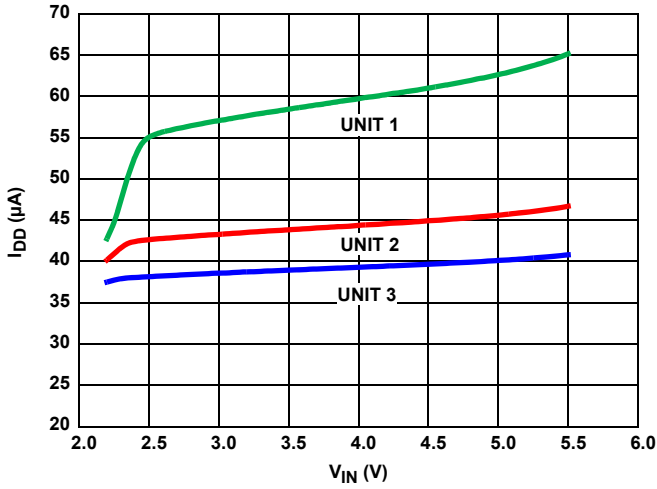


FIGURE 2. I_{IN} vs V_{IN} , THREE UNITS

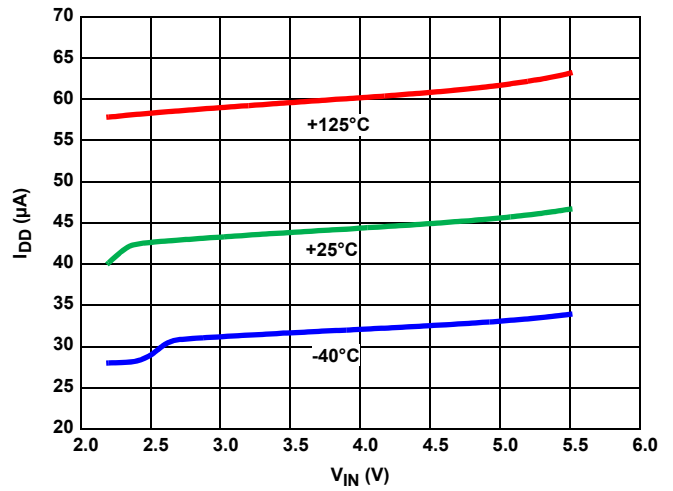


FIGURE 3. I_{IN} vs V_{IN} , OVER-TEMPERATURE

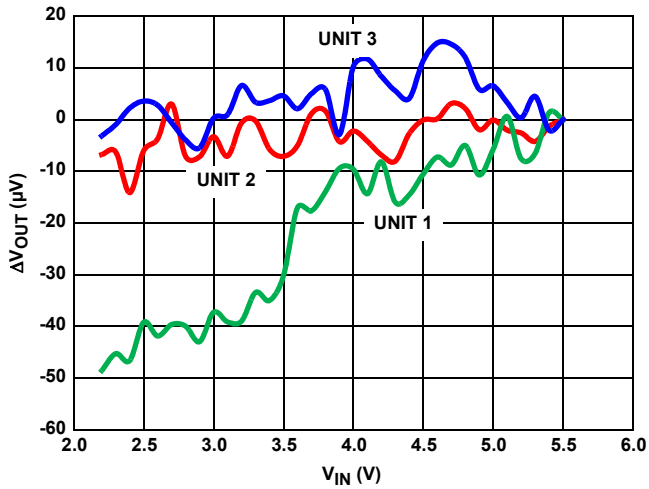


FIGURE 4. LINE REGULATION, THREE UNITS

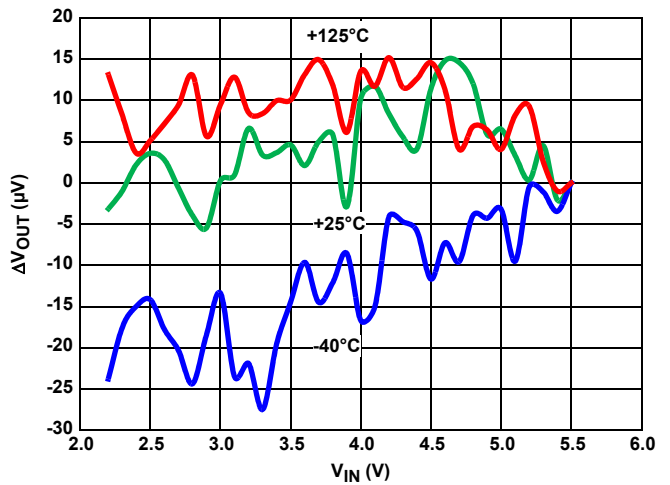


FIGURE 5. LINE REGULATION OVER-TEMPERATURE

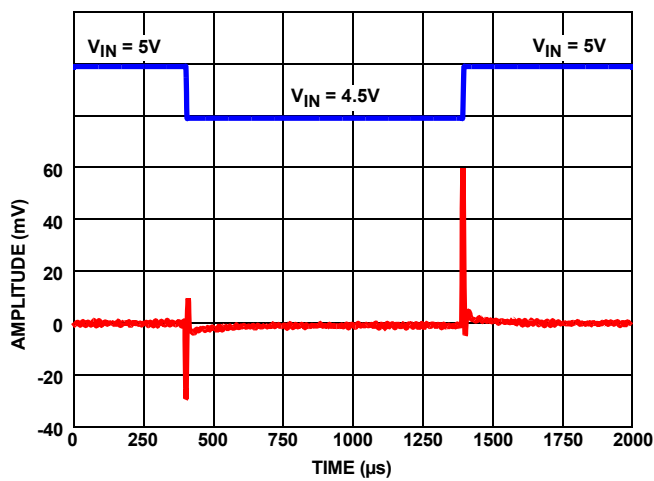


FIGURE 6. LINE TRANSIENT RESPONSE WITH $0.22\mu F$ LOAD

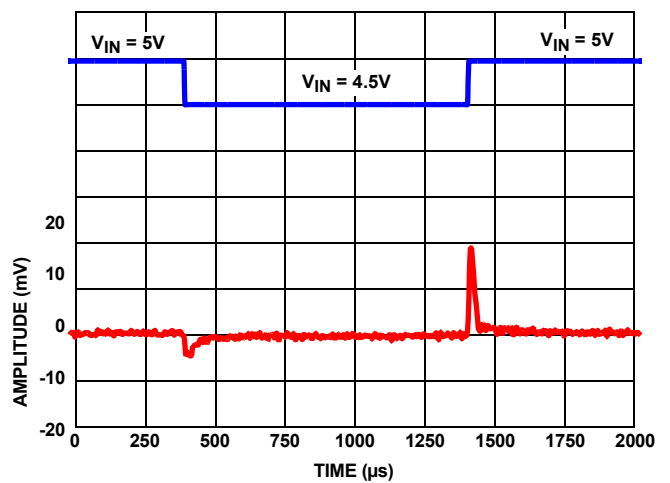


FIGURE 7. LINE TRANSIENT RESPONSE WITH $10\mu F$ LOAD

Typical Performance Characteristics Curves ($V_{OUT} = 1.024V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

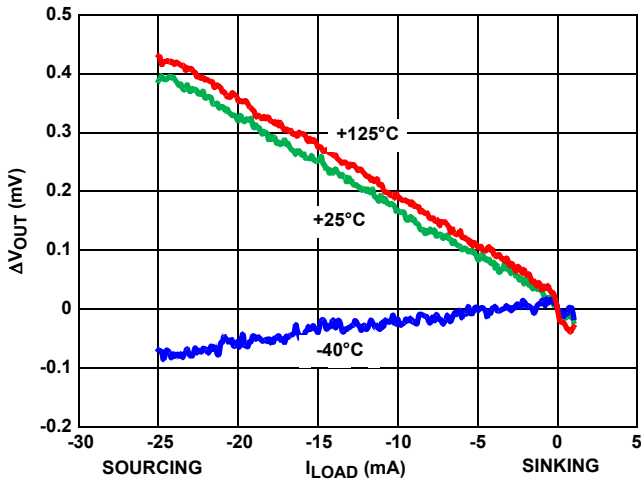


FIGURE 8. LOAD REGULATION OVER-TEMPERATURE

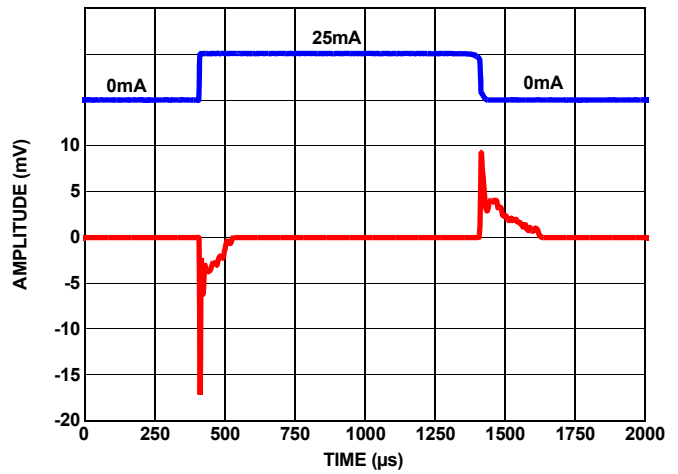


FIGURE 9. LOAD TRANSIENT RESPONSE AT 25mA LOAD AT 1µF

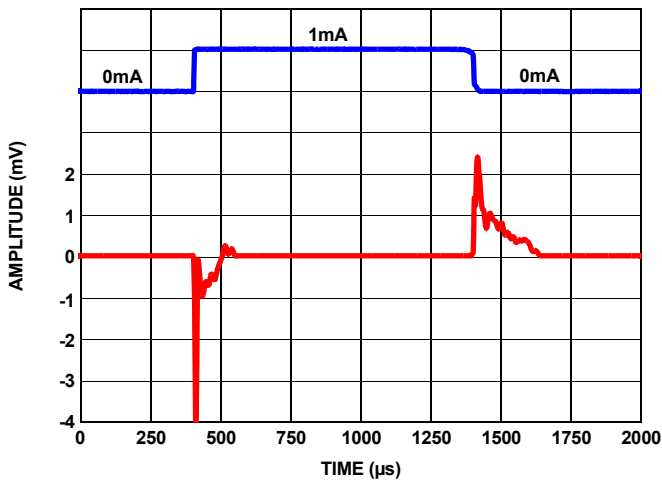


FIGURE 10. LOAD TRANSIENT RESPONSE AT 1mA LOAD AT 1µF

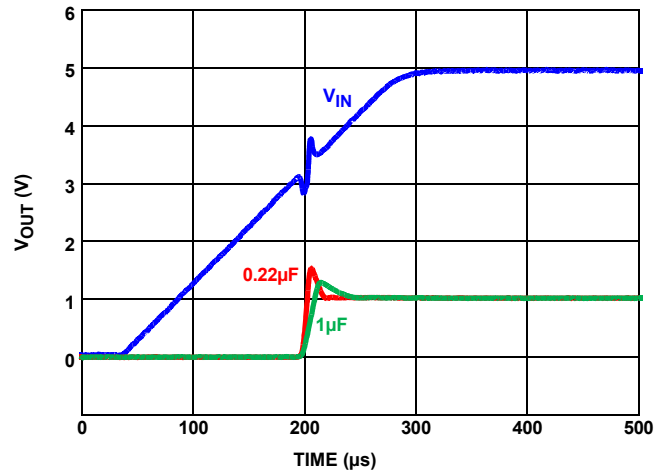


FIGURE 11. TURN-ON TIME

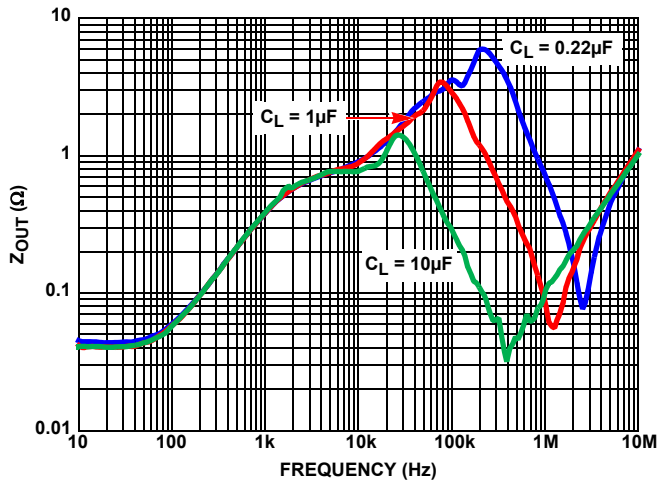


FIGURE 12. Z_{OUT} vs FREQUENCY

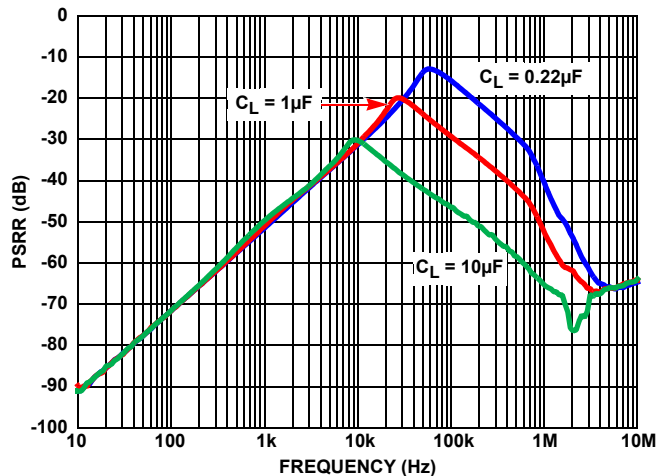


FIGURE 13. RIPPLE REJECTION AT DIFFERENT CAPACITIVE LOADS

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Typical Performance Characteristics Curves ($V_{OUT} = 1.024V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

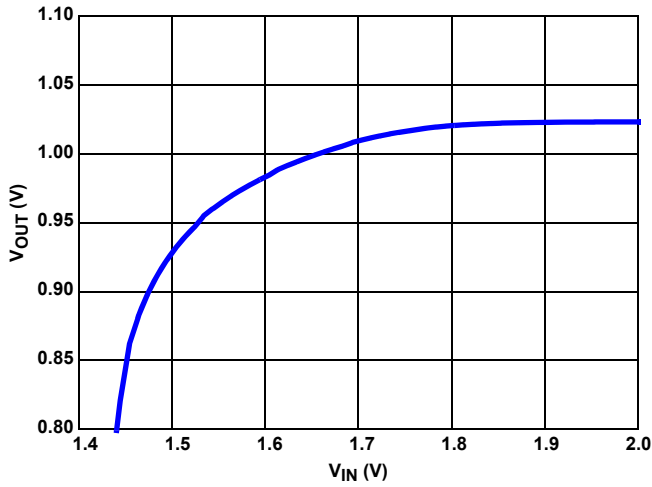


FIGURE 14. DROPOUT (10mA SOURCED LOAD)

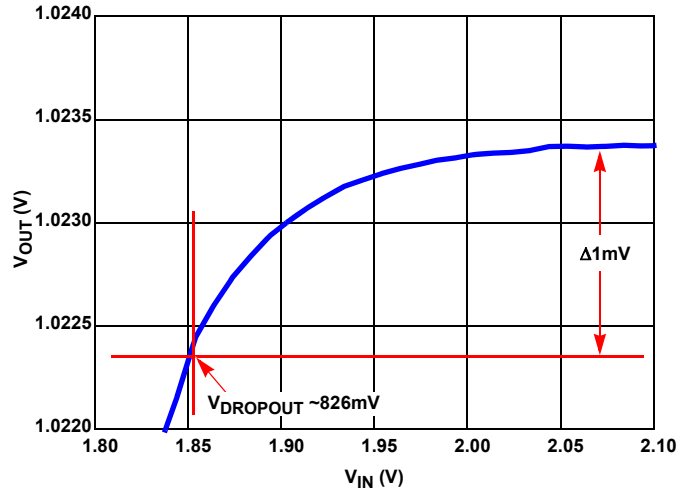


FIGURE 15. DROPOUT ZOOMED (10mA SOURCED LOAD)

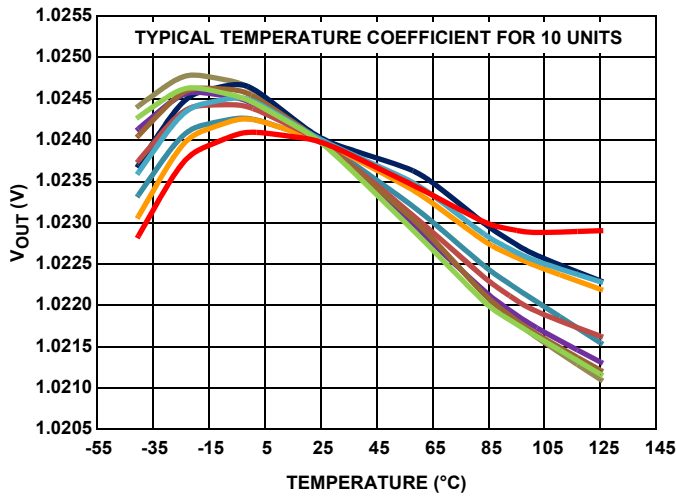


FIGURE 16. V_{OUT} vs TEMPERATURE

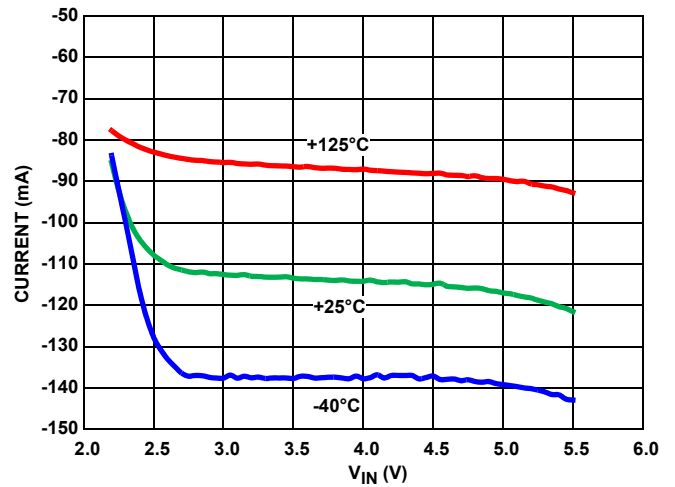


FIGURE 17. SHORT CIRCUIT TO GND

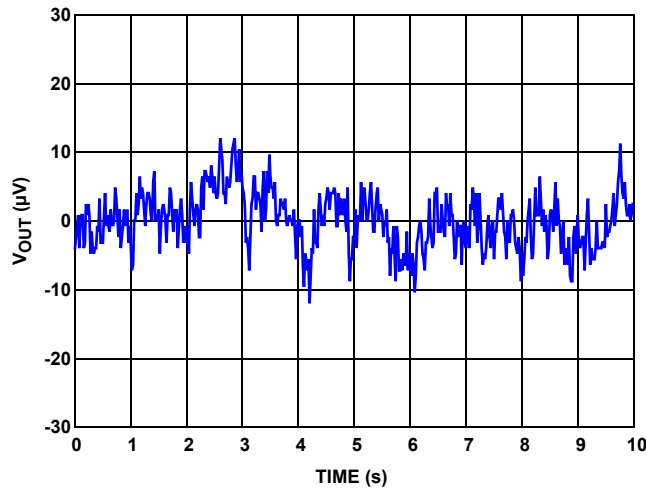


FIGURE 18. V_{OUT} vs NOISE, 0.1Hz TO 10Hz

Typical Performance Characteristics Curves ($V_{OUT} = 1.25V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified.

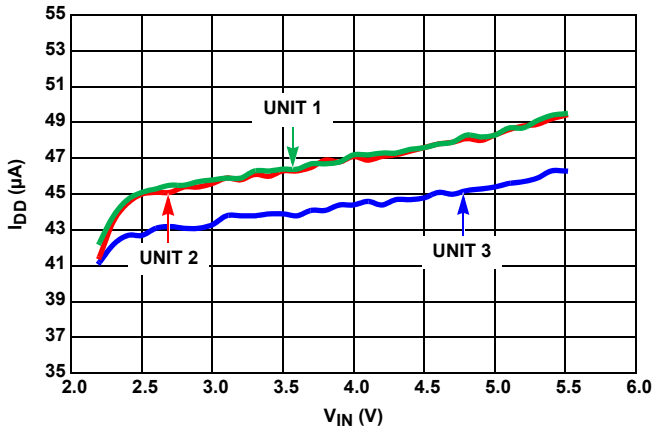


FIGURE 19. I_{IN} vs V_{IN} , THREE UNITS

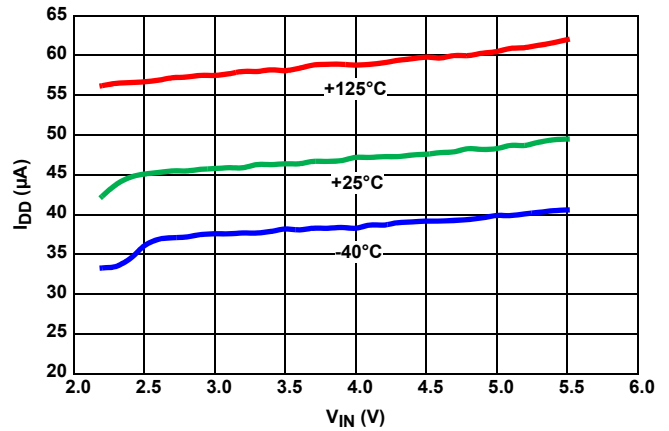


FIGURE 20. I_{IN} vs V_{IN} , OVER-TEMPERATURE

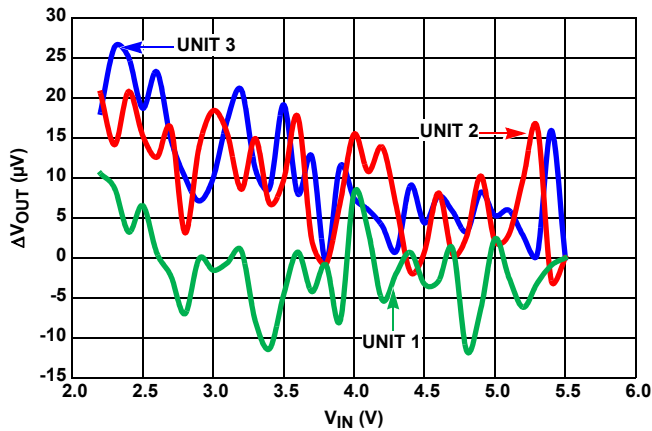


FIGURE 21. LINE REGULATION, THREE UNITS

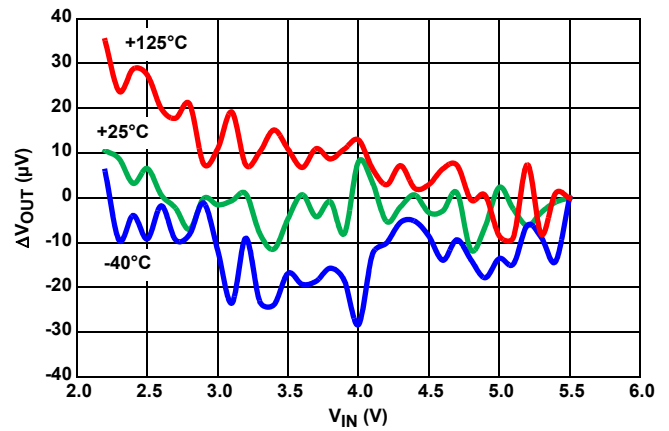


FIGURE 22. LINE REGULATION OVER-TEMPERATURE

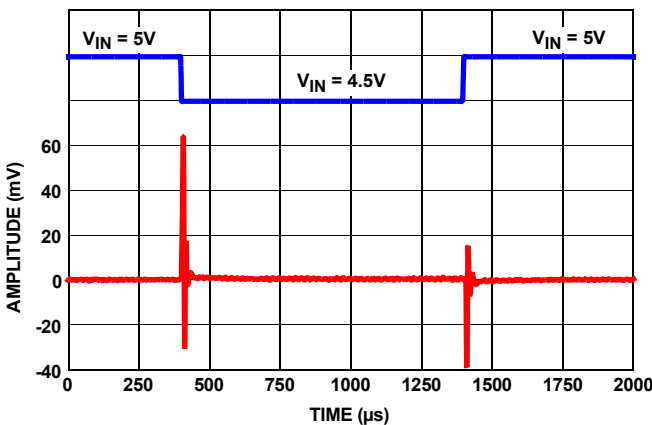


FIGURE 23. LINE TRANSIENT RESPONSE WITH $0.1\mu F$ LOAD

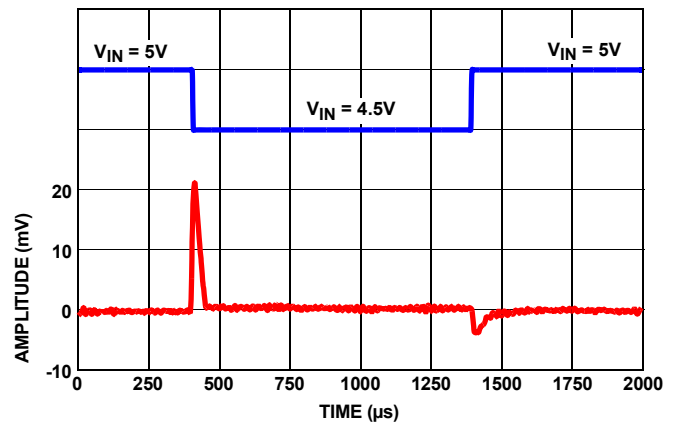


FIGURE 24. LINE TRANSIENT RESPONSE WITH $10\mu F$ LOAD

Typical Performance Characteristics Curves ($V_{OUT} = 1.25V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

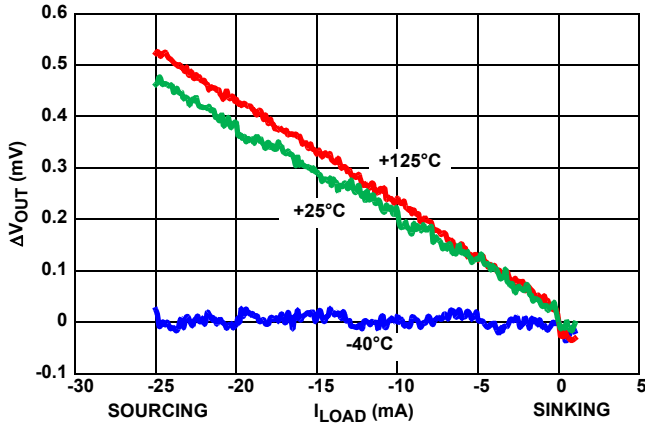


FIGURE 25. LOAD REGULATION OVER-TEMPERATURE

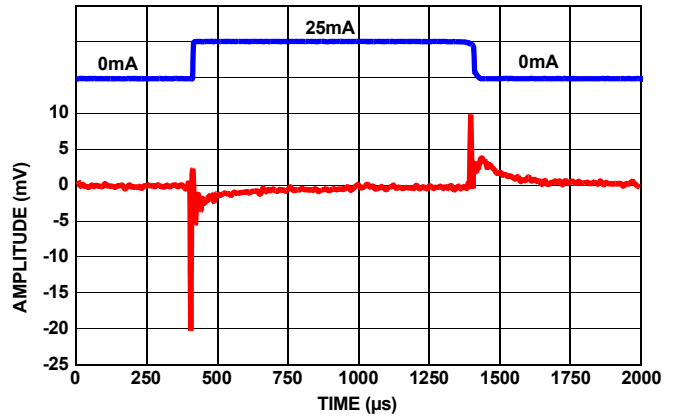


FIGURE 26. LOAD TRANSIENT RESPONSE AT 25mA LOAD AT 1 μ F

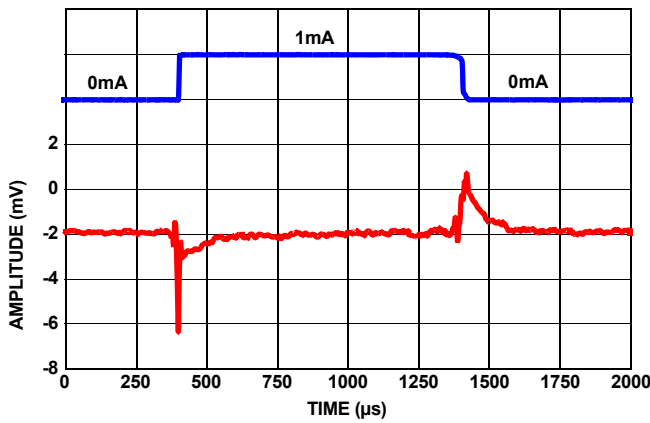


FIGURE 27. LOAD TRANSIENT RESPONSE AT 1mA LOAD AT 1 μ F

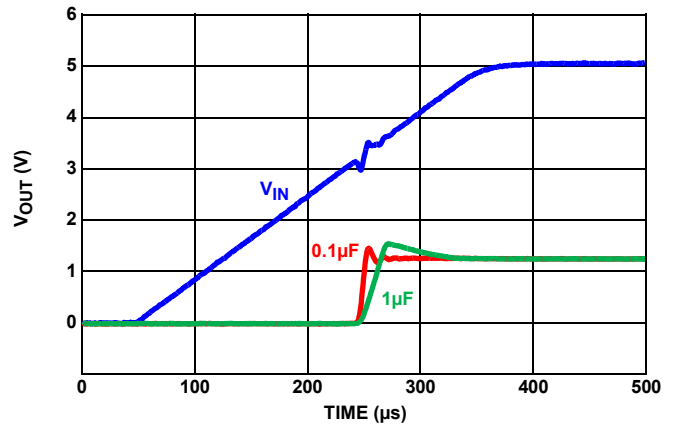


FIGURE 28. TURN-ON TIME

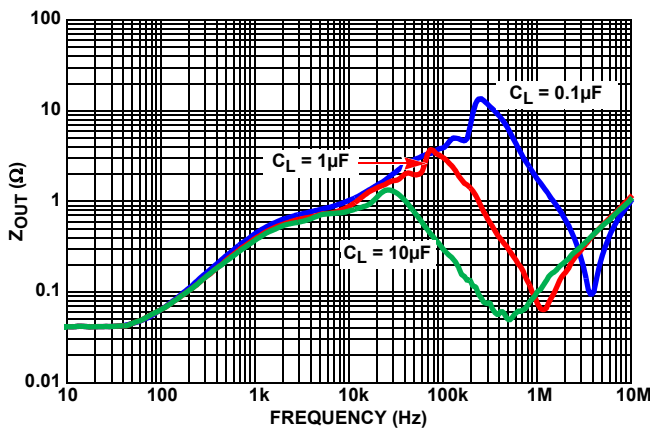


FIGURE 29. Z_{OUT} vs FREQUENCY

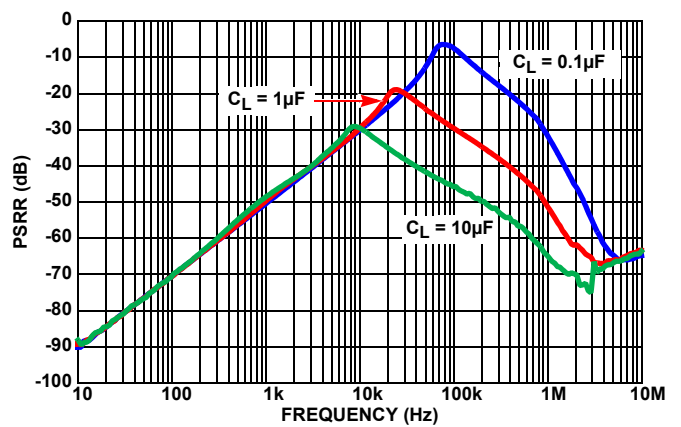


FIGURE 30. RIPPLE REJECTION AT DIFFERENT CAPACITIVE LOADS

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Typical Performance Characteristics Curves ($V_{OUT} = 1.25V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

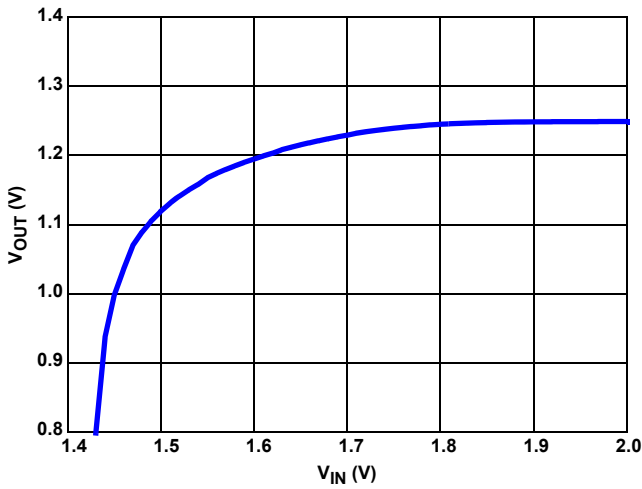


FIGURE 31. DROPOUT (10mA SOURCED LOAD)

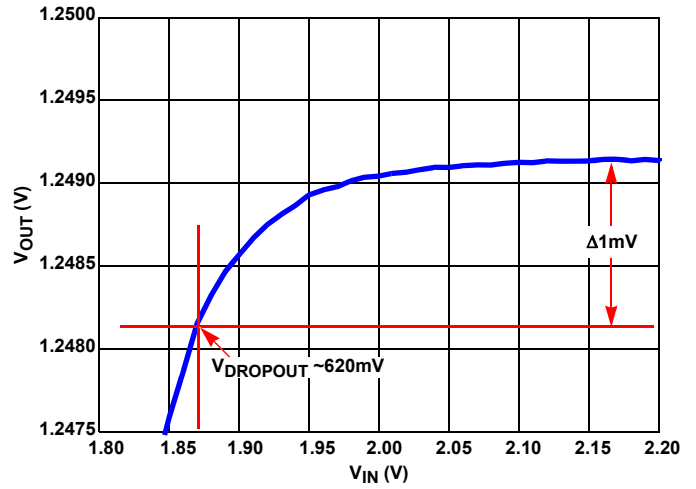


FIGURE 32. DROPOUT ZOOMED (10mA SOURCED LOAD)

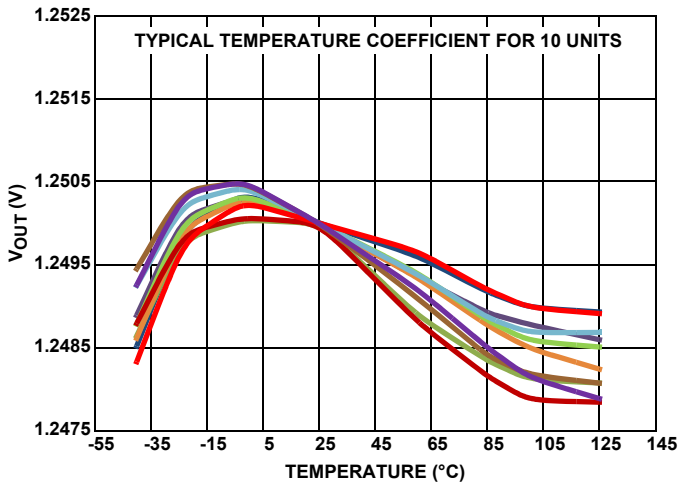


FIGURE 33. V_{OUT} vs TEMPERATURE

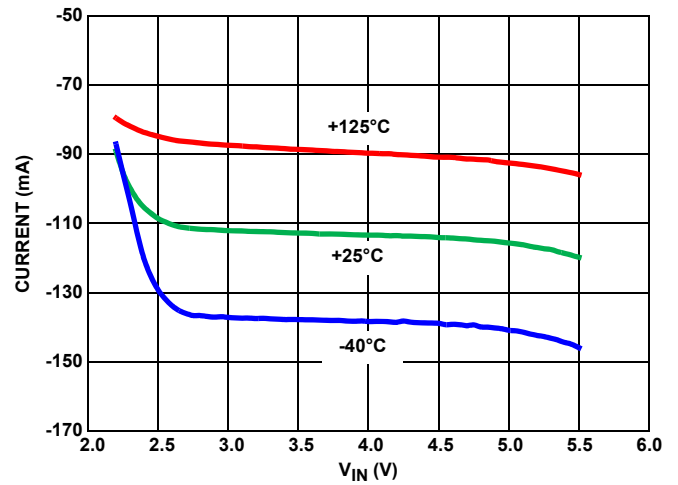


FIGURE 34. SHORT-CIRCUIT TO GND

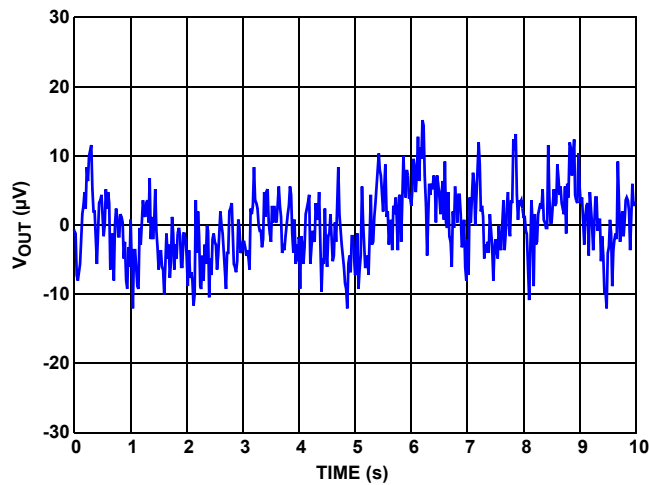


FIGURE 35. V_{OUT} vs NOISE, 0.1Hz TO 10Hz

Typical Performance Characteristics Curves ($V_{OUT} = 1.5V$) $V_{IN} = 3.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified.

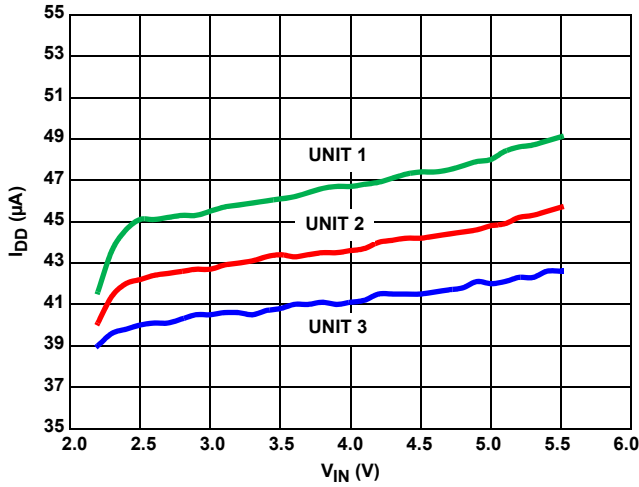


FIGURE 36. I_{IN} vs V_{IN} , THREE UNITS

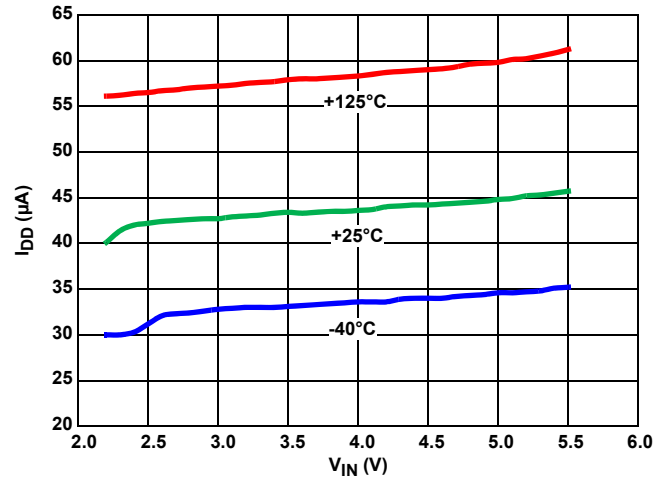


FIGURE 37. I_{IN} vs V_{IN} , OVER-TEMPERATURE

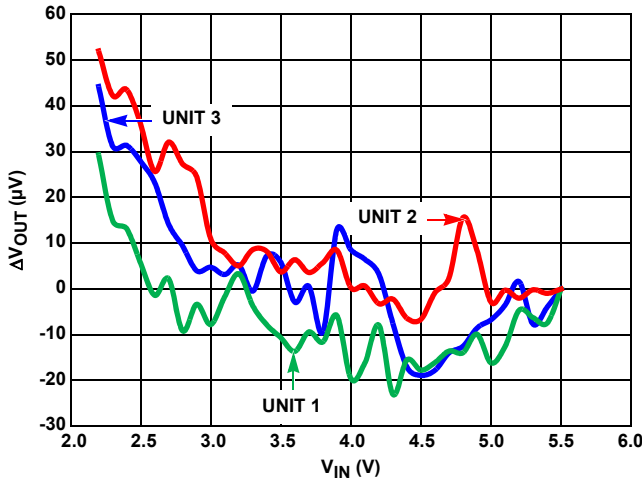


FIGURE 38. LINE REGULATION, THREE UNITS

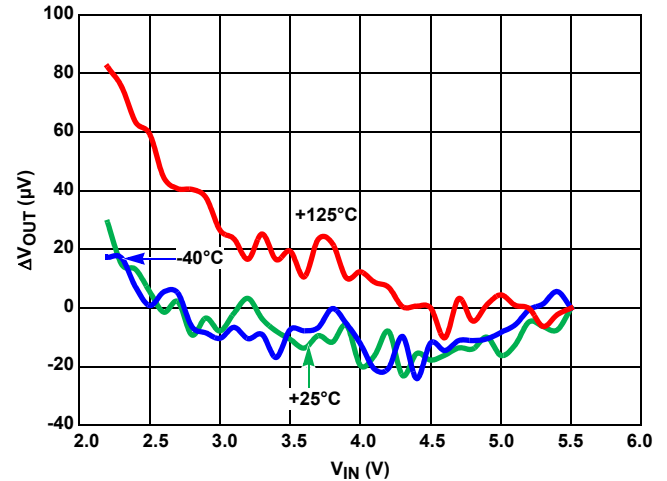


FIGURE 39. LINE REGULATION OVER-TEMPERATURE

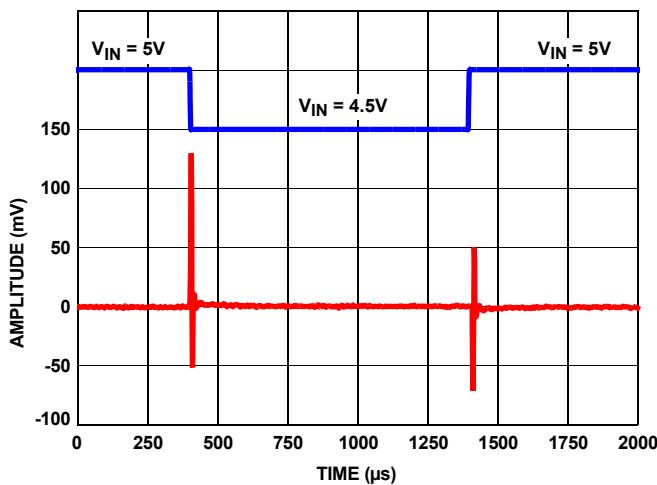


FIGURE 40. LINE TRANSIENT RESPONSE WITH $0.1\mu F$ LOAD

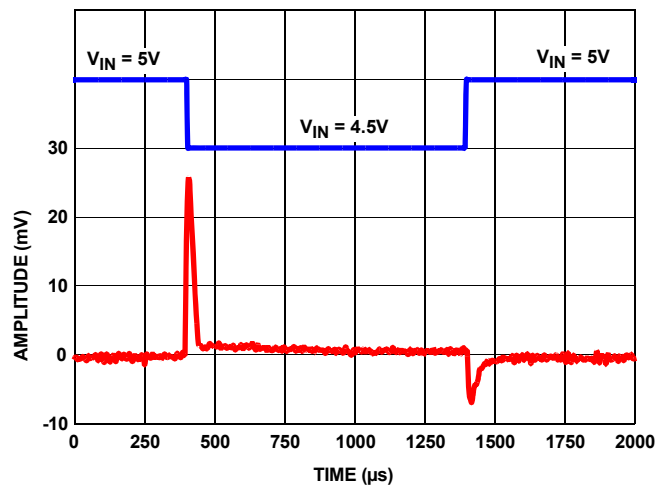


FIGURE 41. LINE TRANSIENT RESPONSE WITH $10\mu F$ LOAD

Typical Performance Characteristics Curves ($V_{OUT} = 1.5V$) $V_{IN} = 3.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified. (Continued)

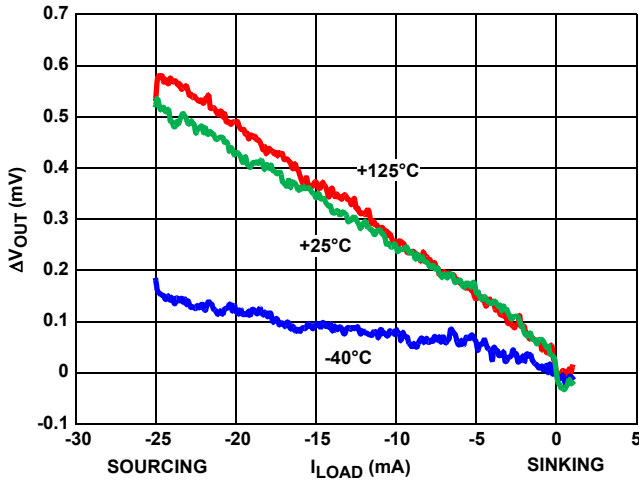


FIGURE 42. LOAD REGULATION OVER-TEMPERATURE

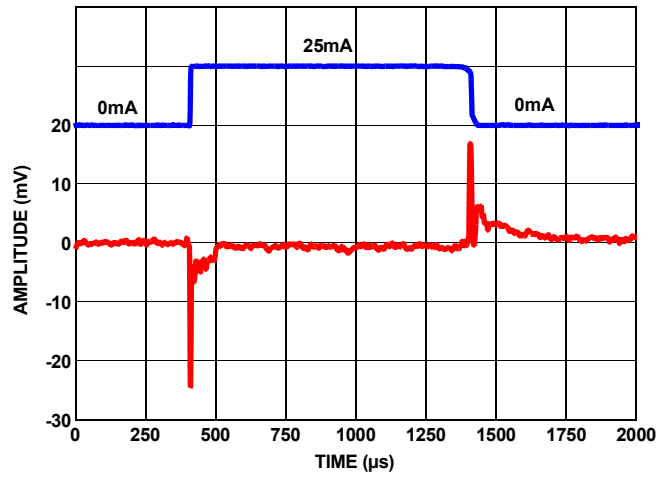


FIGURE 43. LOAD TRANSIENT RESPONSE AT 25mA LOAD AT 1µF

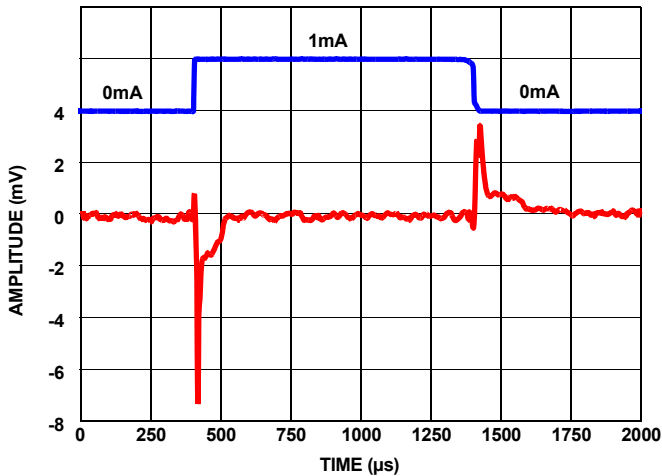


FIGURE 44. LOAD TRANSIENT RESPONSE AT 1mA LOAD AT 1µF

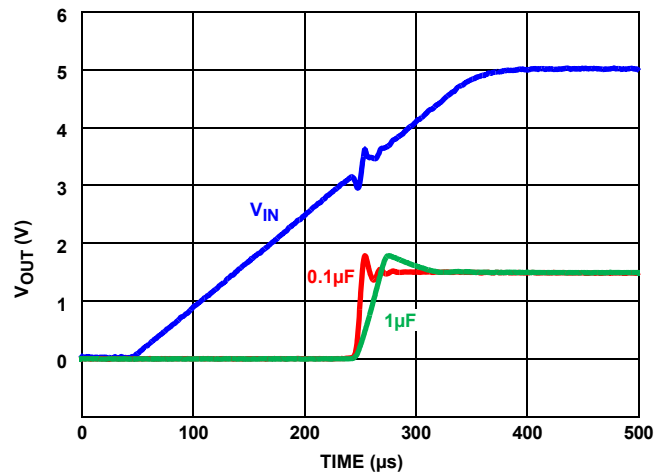


FIGURE 45. TURN-ON TIME

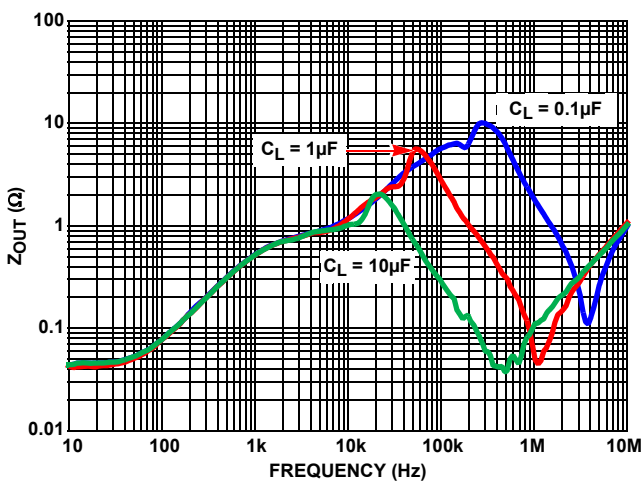


FIGURE 46. Z_{OUT} vs FREQUENCY

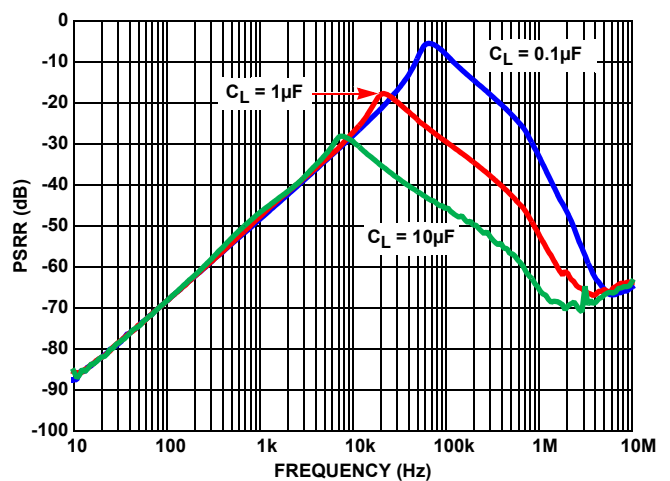


FIGURE 47. RIPPLE REJECTION AT DIFFERENT CAPACITIVE LOADS

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Typical Performance Characteristics Curves ($V_{OUT} = 1.5V$) $V_{IN} = 3.0V, I_{OUT} = 0mA, T_A = +25^\circ C$ unless otherwise specified. (Continued)

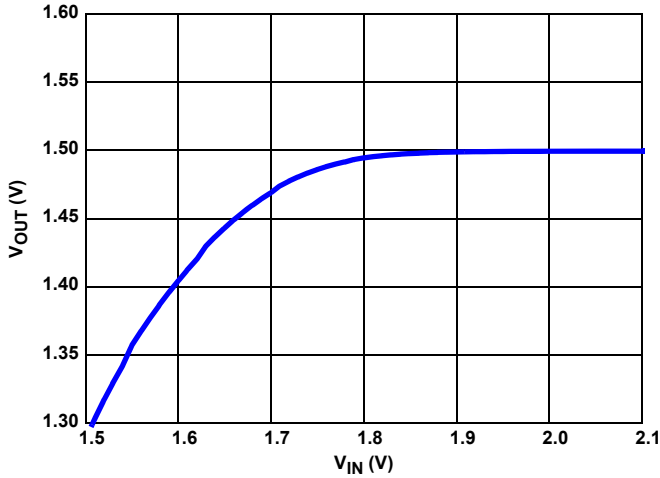


FIGURE 48. DROPOUT (10mA SOURCED LOAD)

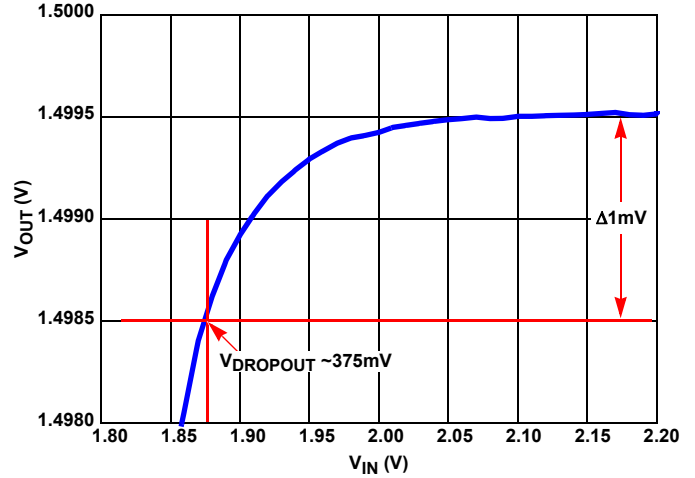


FIGURE 49. DROPOUT ZOOMED (10mA SOURCED LOAD)

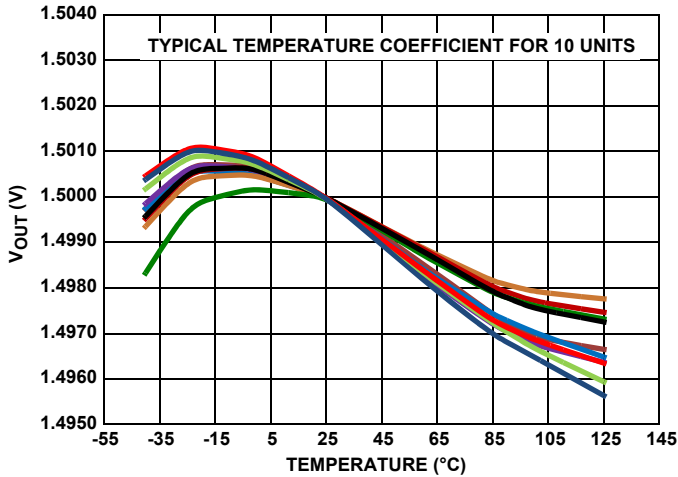


FIGURE 50. V_{OUT} vs TEMPERATURE

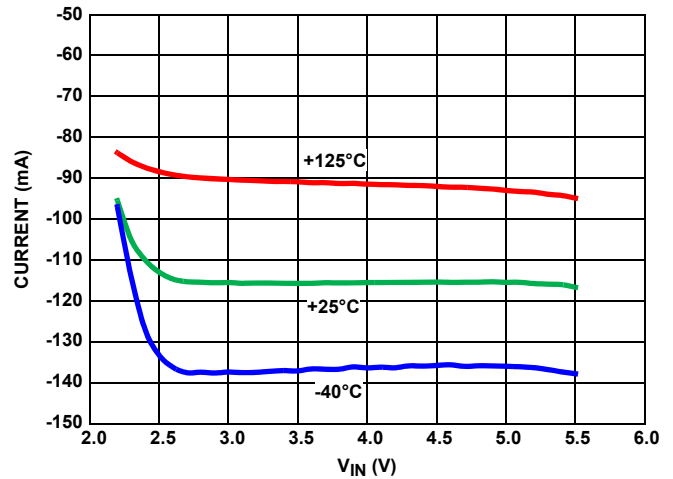


FIGURE 51. SHORT-CIRCUIT TO GND

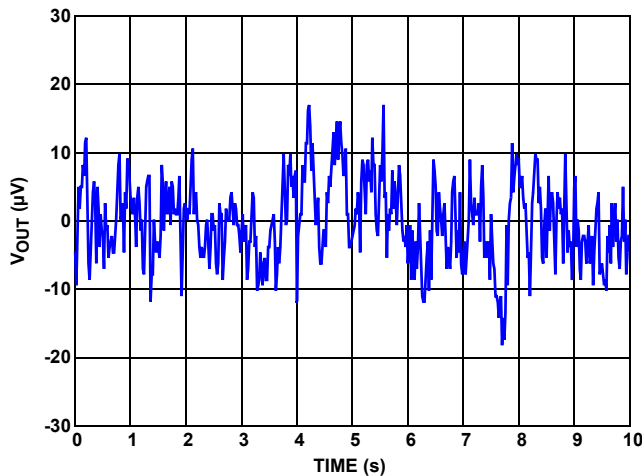


FIGURE 52. V_{OUT} vs NOISE, 0.1Hz TO 10Hz

Typical Performance Characteristics Curves ($V_{OUT} = 2.048V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified.

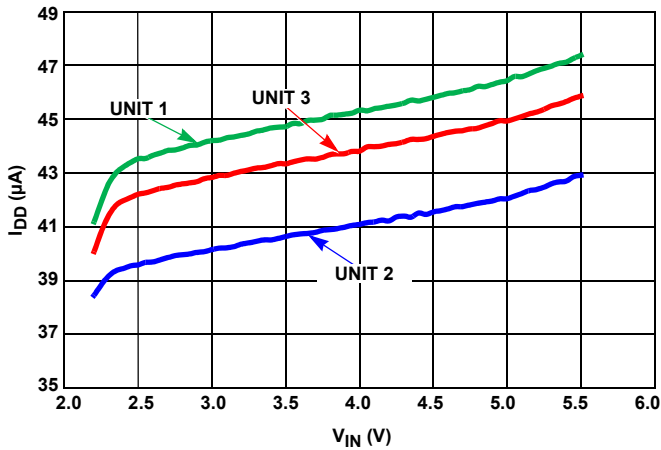


FIGURE 53. I_{IN} vs V_{IN} , THREE UNITS

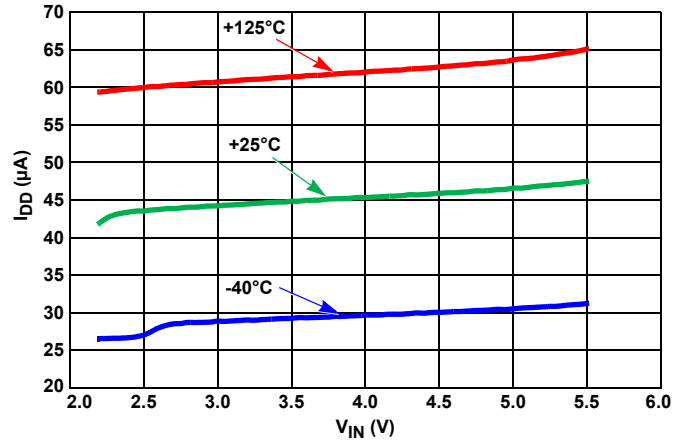


FIGURE 54. I_{IN} vs V_{IN} , OVER-TEMPERATURE

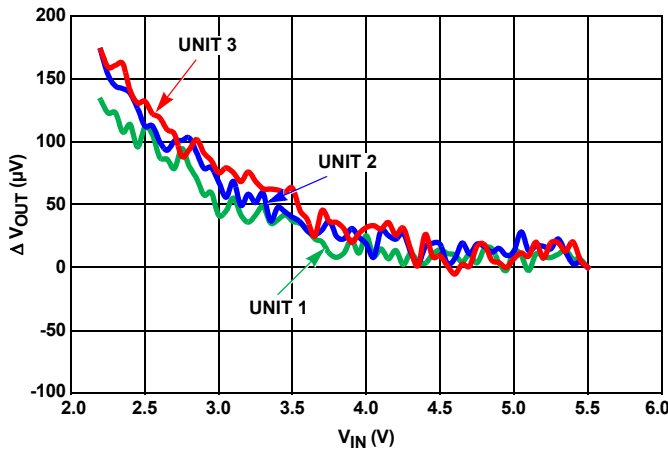


FIGURE 55. LINE REGULATION, THREE UNITS

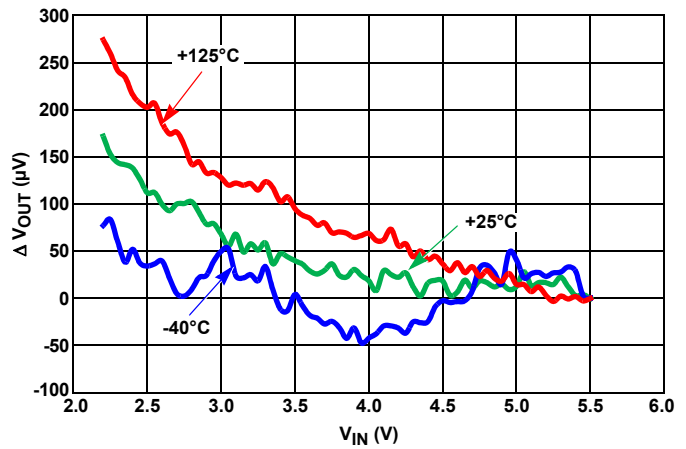


FIGURE 56. LINE REGULATION OVER-TEMPERATURE

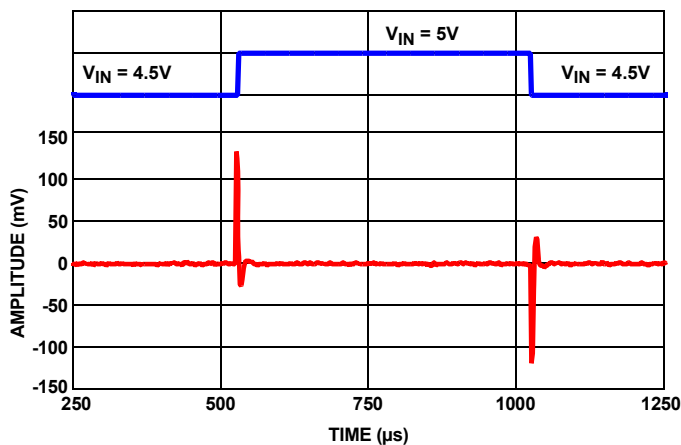


FIGURE 57. LINE TRANSIENT RESPONSE WITH $0.1\mu F$ LOAD

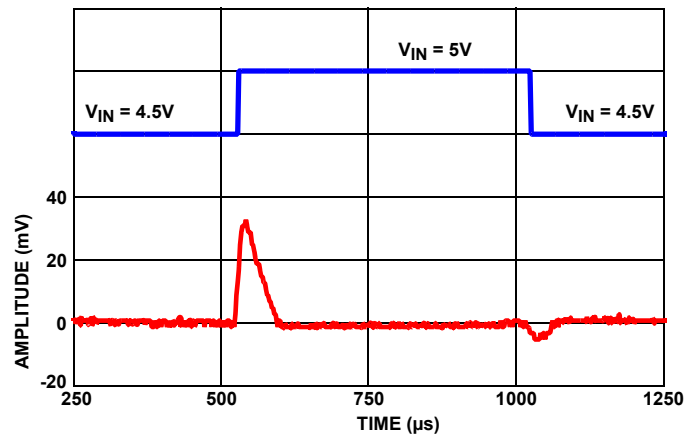


FIGURE 58. LINE TRANSIENT RESPONSE WITH $10\mu F$ LOAD

Typical Performance Characteristics Curves ($V_{OUT} = 2.048V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

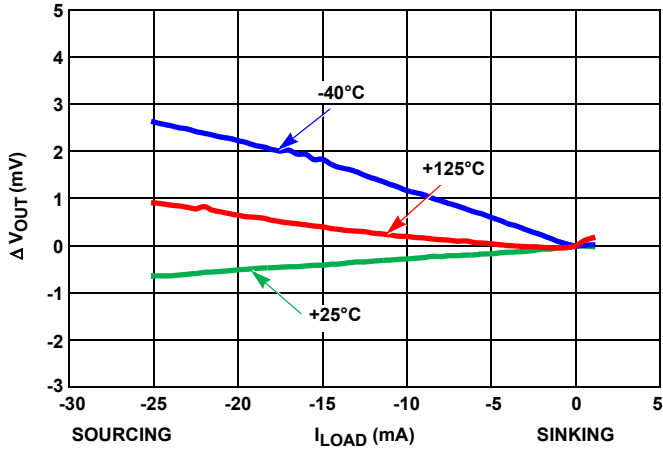


FIGURE 59. LOAD REGULATION OVER-TEMPERATURE

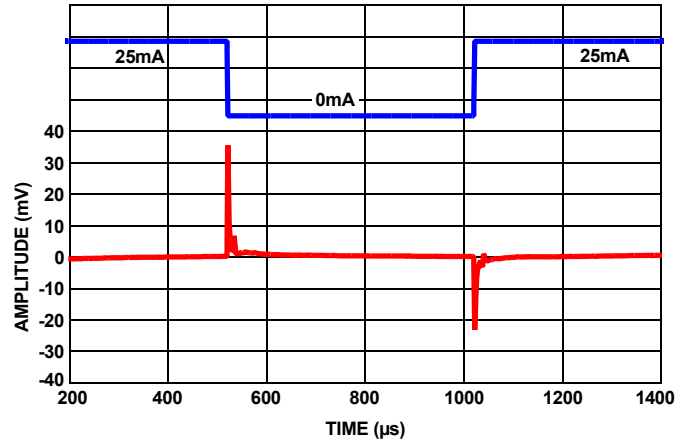


FIGURE 60. LOAD TRANSIENT RESPONSE AT 25mA LOAD AT 1µF

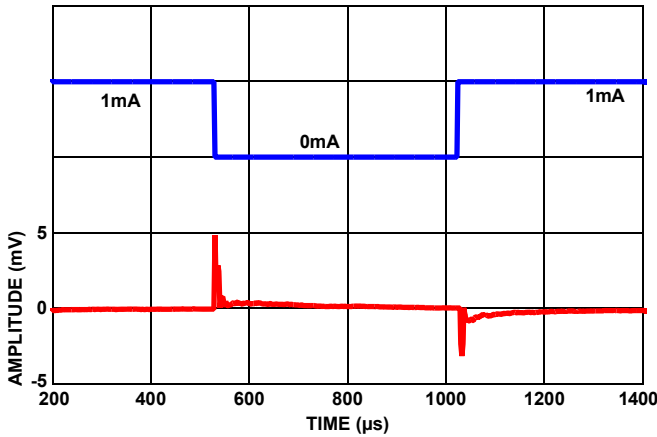


FIGURE 61. LOAD TRANSIENT RESPONSE AT 1mA LOAD AT 1µF

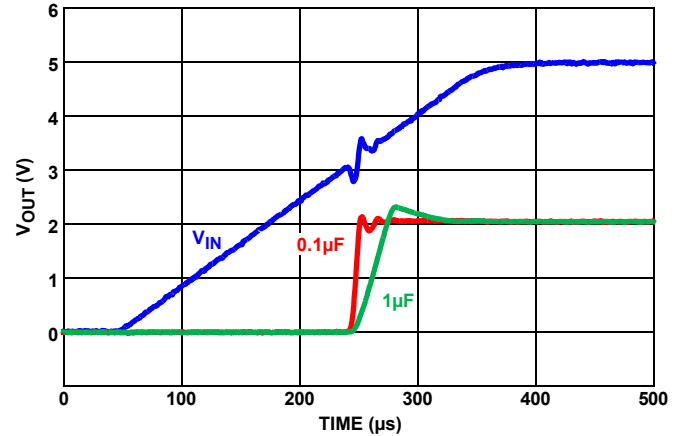


FIGURE 62. TURN-ON TIME

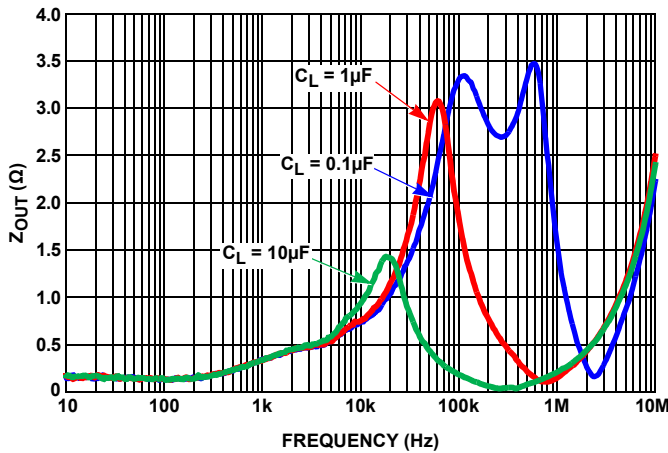


FIGURE 63. Z_{OUT} vs FREQUENCY

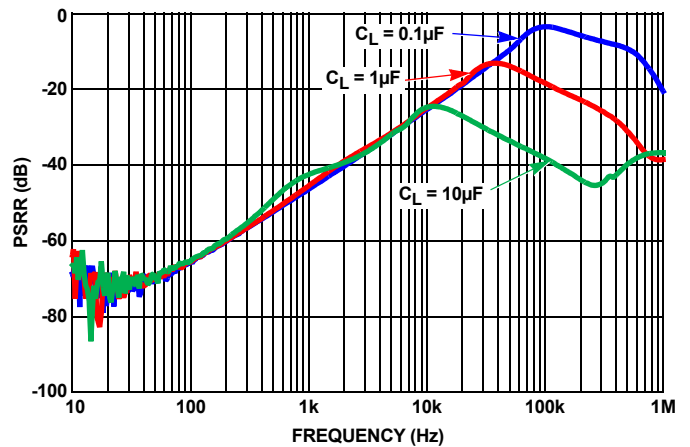


FIGURE 64. RIPPLE REJECTION AT DIFFERENT CAPACITIVE LOADS

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Typical Performance Characteristics Curves ($V_{OUT} = 2.048V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

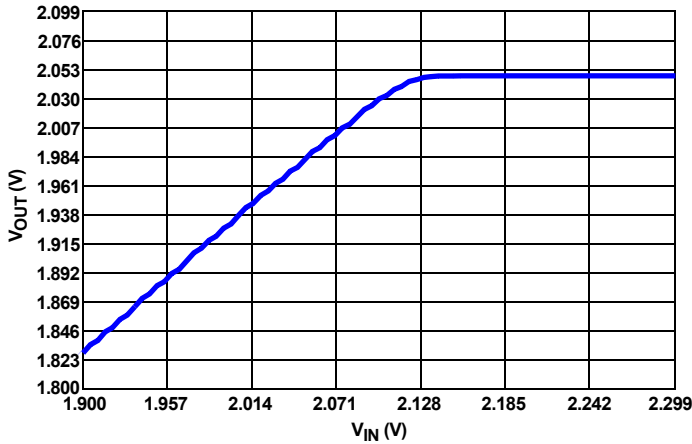


FIGURE 65. DROPOUT (10mA SOURCED LOAD)

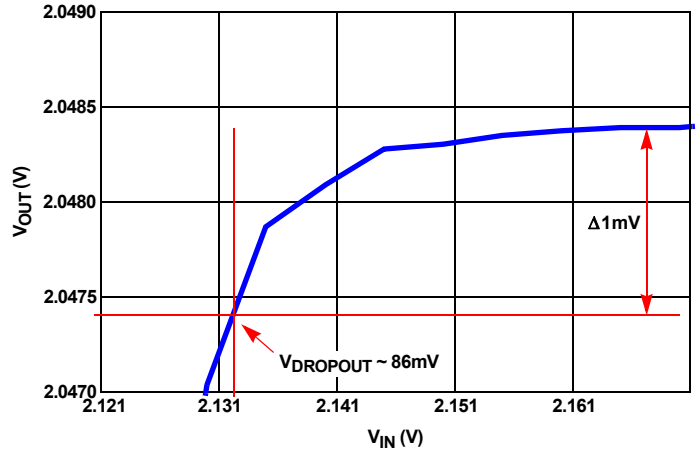


FIGURE 66. DROPOUT ZOOMED (10mA SOURCED LOAD)

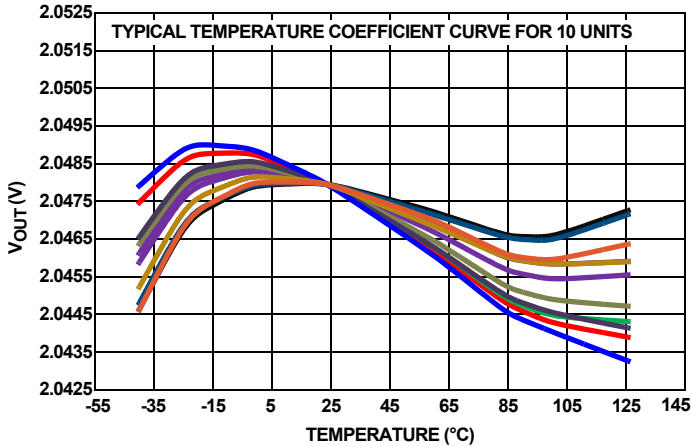


FIGURE 67. V_{OUT} vs TEMPERATURE

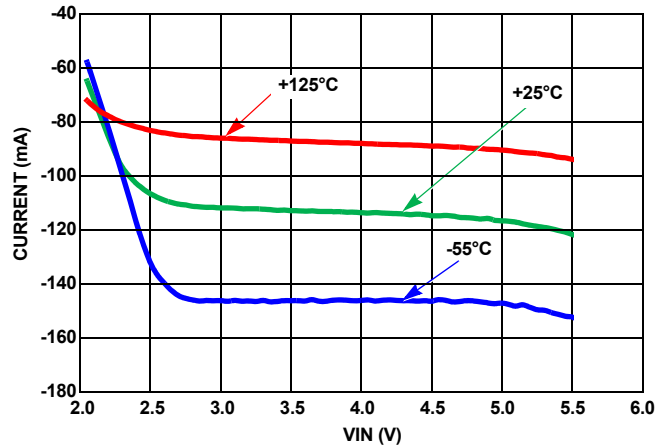


FIGURE 68. SHORT CIRCUIT TO GND

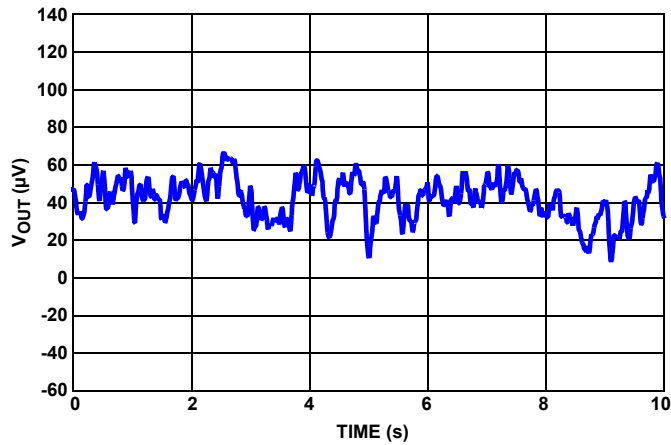


FIGURE 69. V_{OUT} vs NOISE, 0.1Hz TO 10Hz

Typical Performance Characteristics Curves ($V_{OUT} = 2.5V$) $V_{IN} = 3.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified.

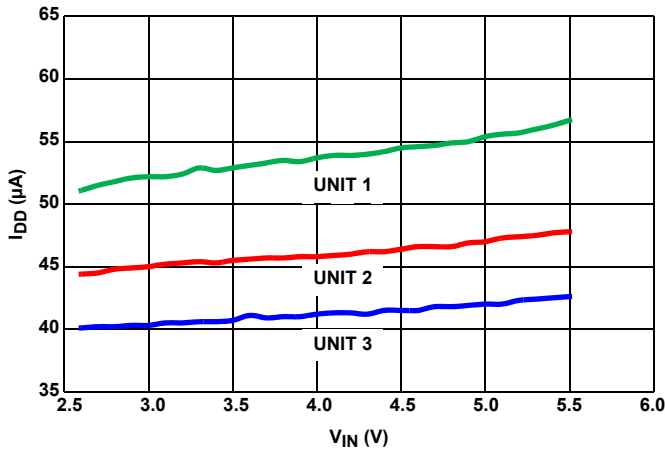


FIGURE 70. I_{DD} vs V_{IN} , THREE UNITS

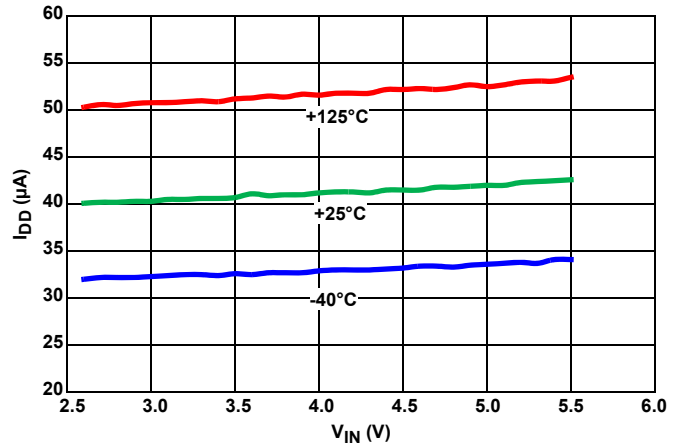


FIGURE 71. I_{DD} vs V_{IN} , OVER-TEMPERATURE

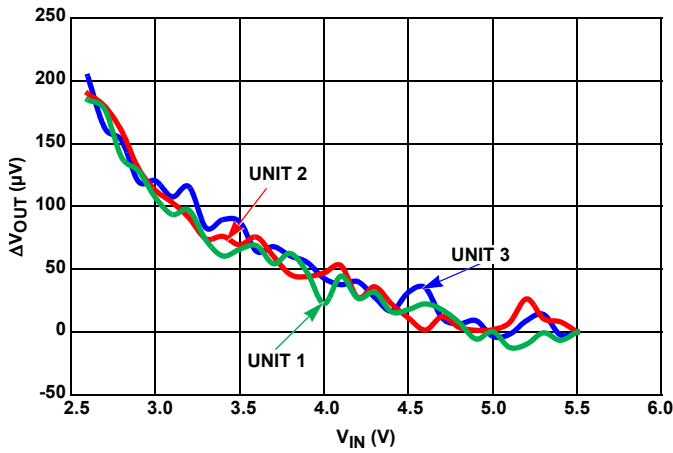


FIGURE 72. LINE REGULATION, THREE UNITS

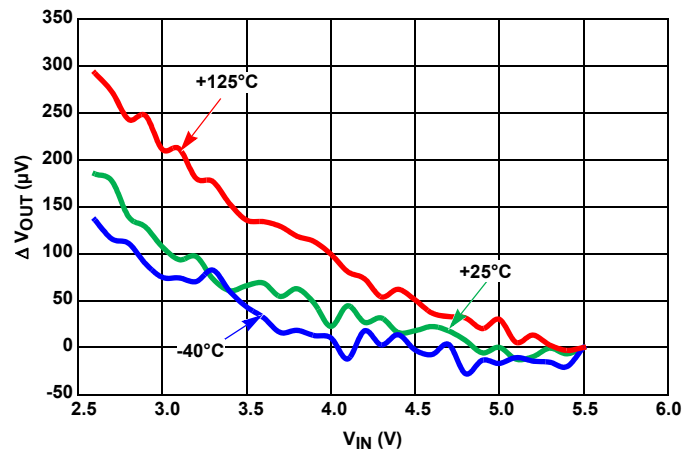


FIGURE 73. LINE REGULATION OVER-TEMPERATURE

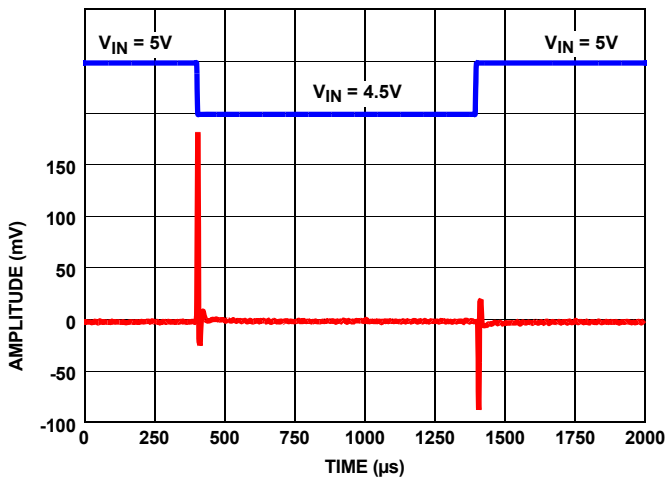


FIGURE 74. LINE TRANSIENT RESPONSE WITH $0.1\mu F$ LOAD

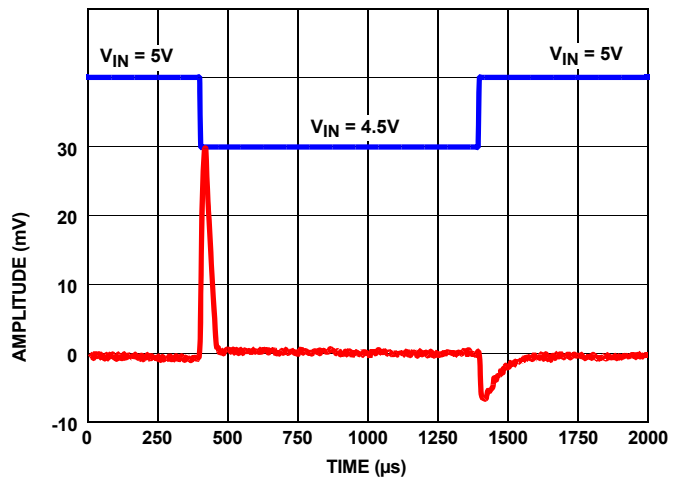


FIGURE 75. LINE TRANSIENT RESPONSE WITH $10\mu F$ LOAD

Typical Performance Characteristics Curves ($V_{OUT} = 2.5V$) $V_{IN} = 3.0V, I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

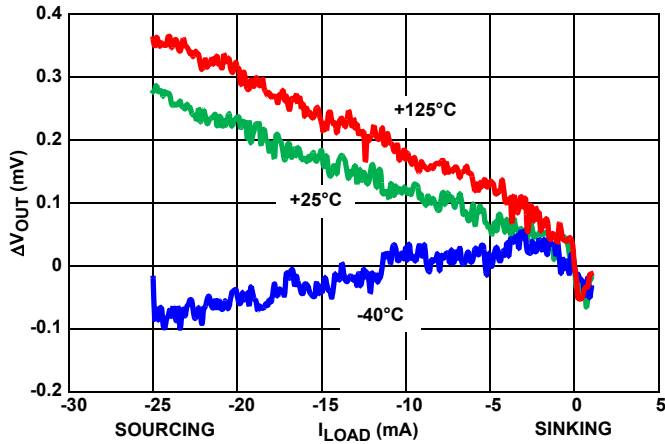


FIGURE 76. LOAD REGULATION OVER-TEMPERATURE

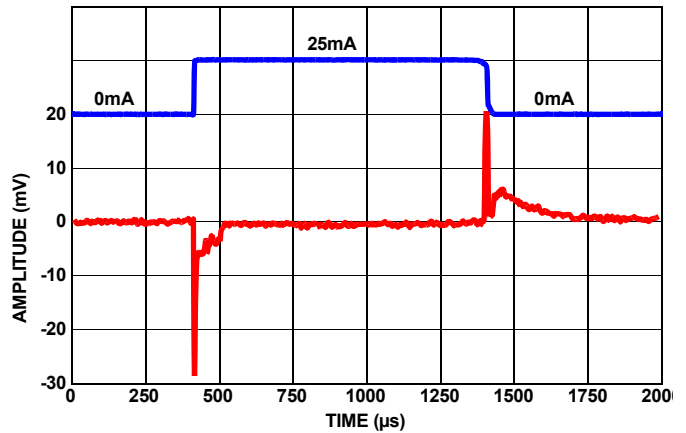


FIGURE 77. LOAD TRANSIENT RESPONSE AT 25mA LOAD AT 1μF

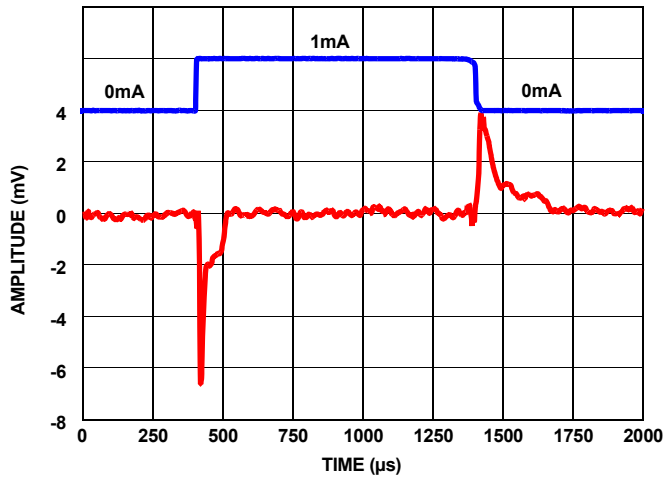


FIGURE 78. LOAD TRANSIENT RESPONSE AT 1mA LOAD AT 1μF

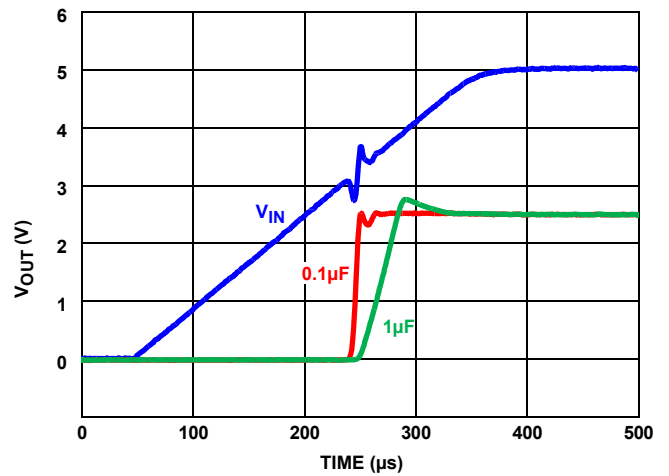


FIGURE 79. TURN-ON TIME

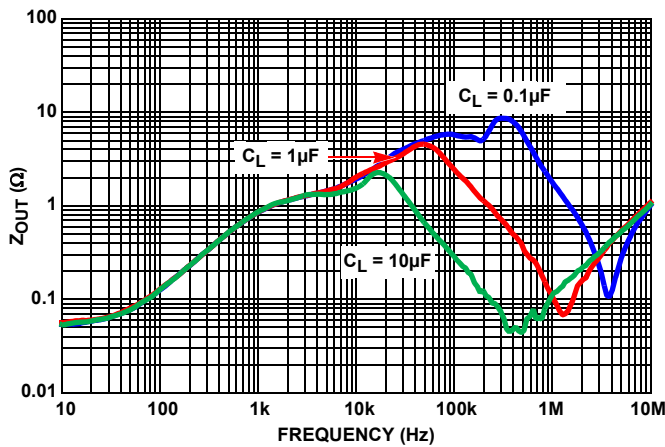


FIGURE 80. Z_{OUT} vs FREQUENCY

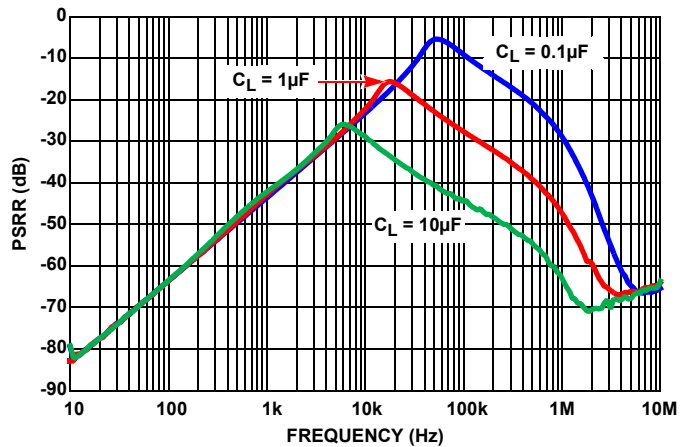


FIGURE 81. RIPPLE REJECTION AT DIFFERENT CAPACITIVE LOADS

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Typical Performance Characteristics Curves ($V_{OUT} = 2.5V$) $V_{IN} = 3.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified. (Continued)

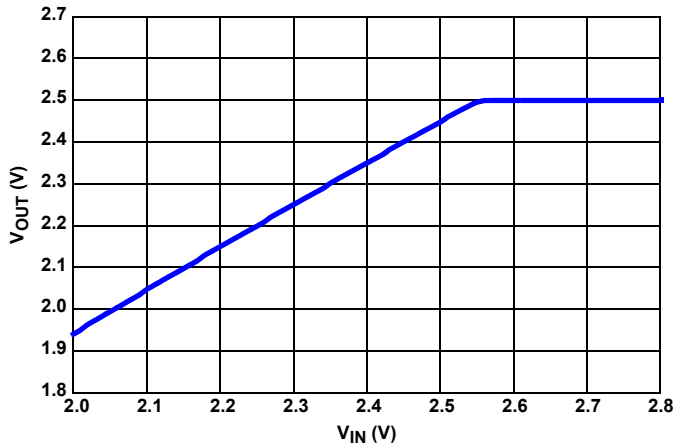


FIGURE 82. DROPOUT (10mA SOURCED LOAD)

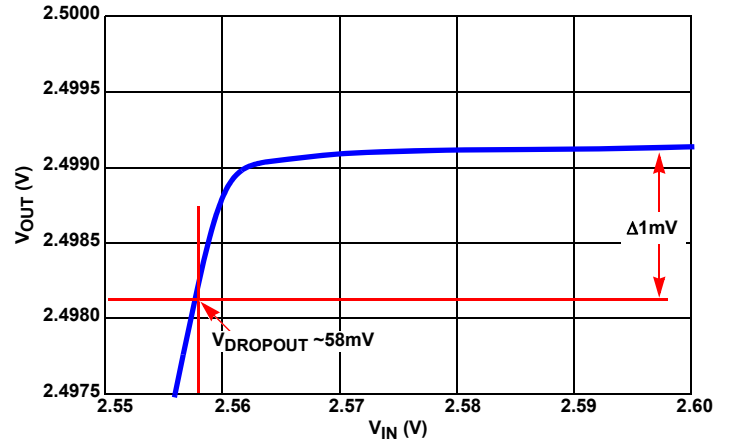


FIGURE 83. DROPOUT ZOOMED (10mA SOURCED LOAD)

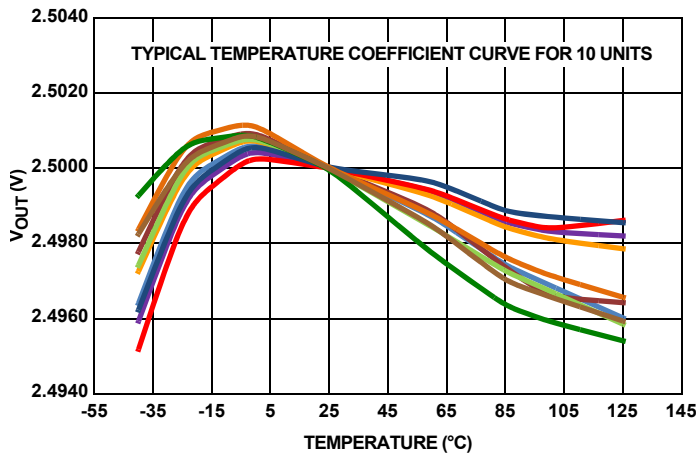


FIGURE 84. V_{OUT} vs TEMPERATURE

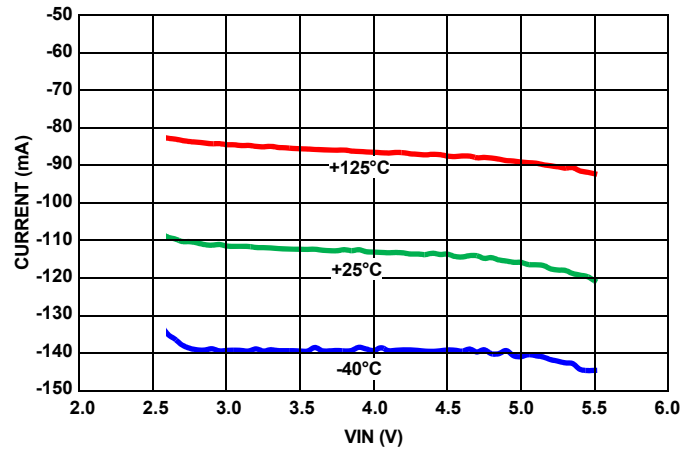


FIGURE 85. SHORT-CIRCUIT TO GND

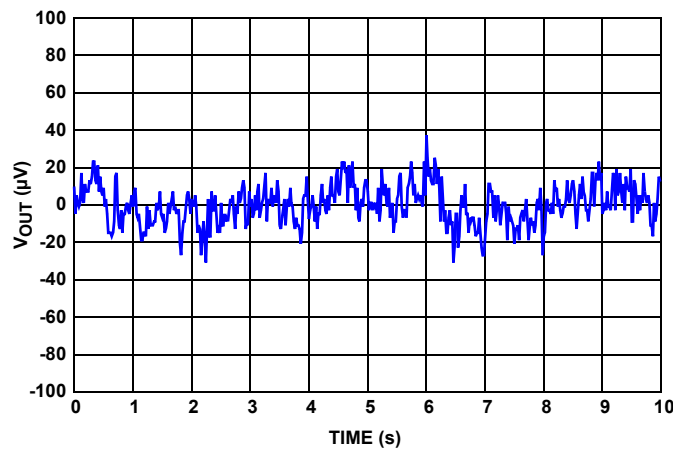


FIGURE 86. V_{OUT} vs NOISE, 0.1Hz TO 10Hz

Typical Performance Characteristics Curves ($V_{OUT} = 3.0V$) $V_{IN} = 5.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified.

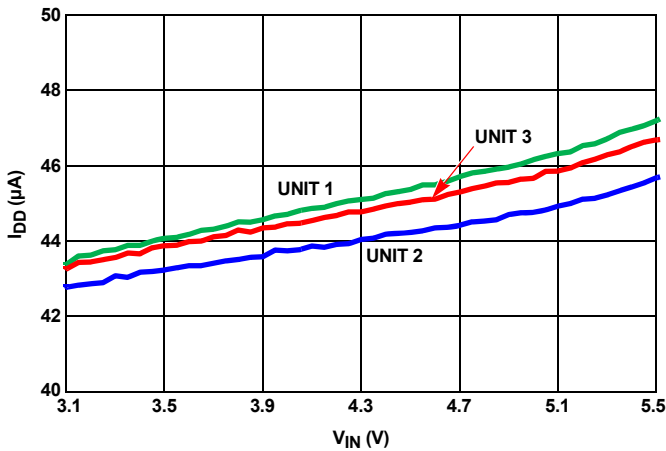


FIGURE 87. I_{IN} vs V_{IN} , THREE UNITS

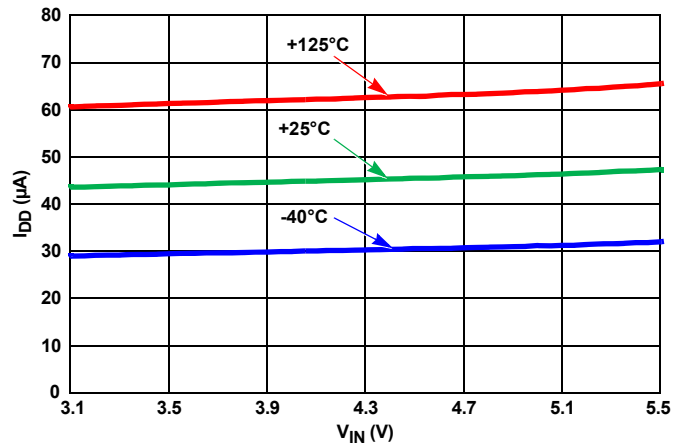


FIGURE 88. I_{IN} vs V_{IN} , OVER-TEMPERATURE

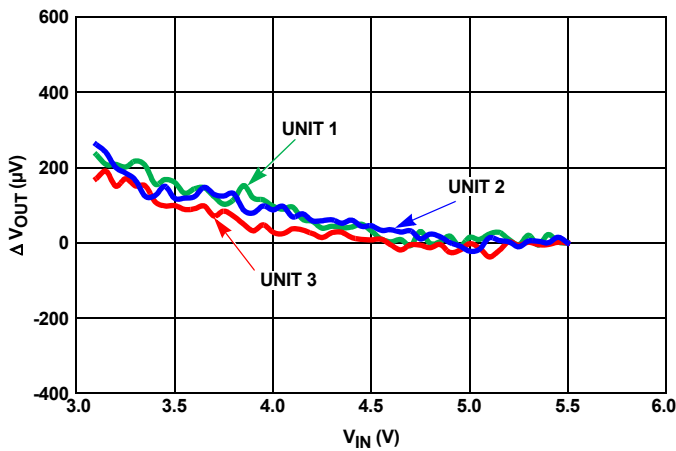


FIGURE 89. LINE REGULATION, THREE UNITS

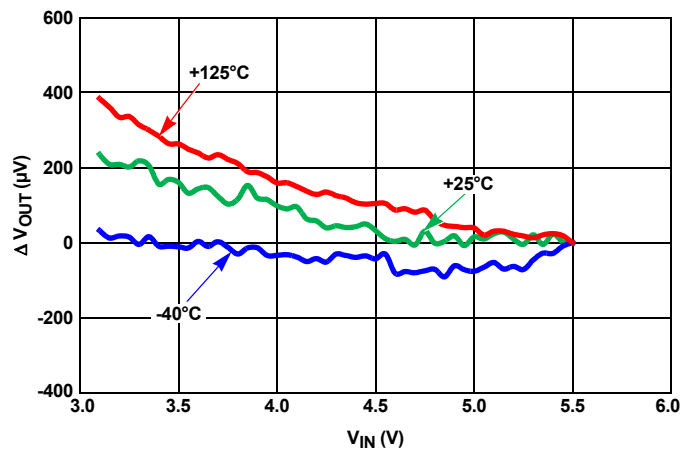


FIGURE 90. LINE REGULATION OVER-TEMPERATURE

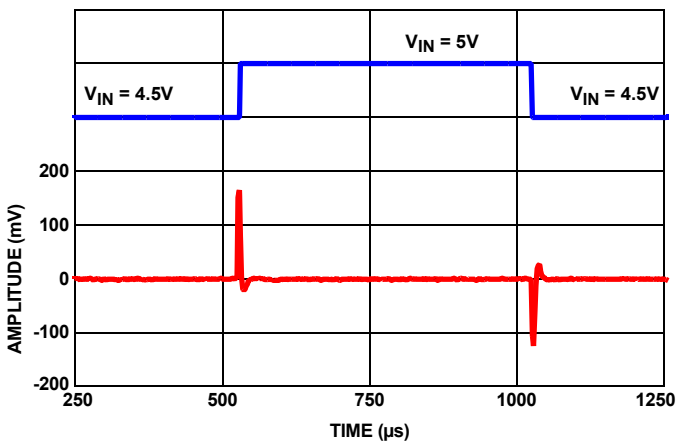


FIGURE 91. LINE TRANSIENT WITH $0.1\mu F$ LOAD

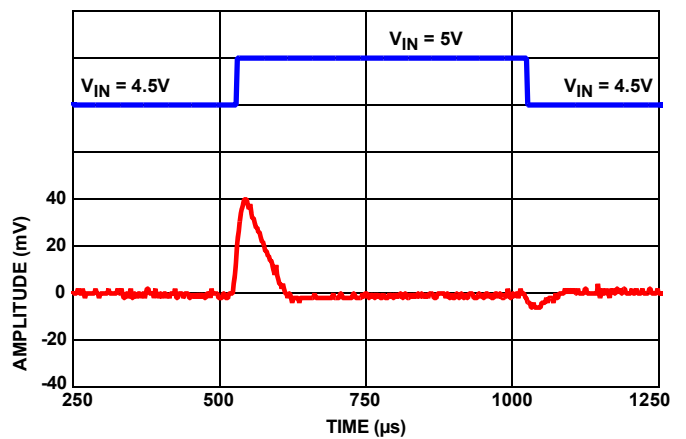


FIGURE 92. LINE TRANSIENT RESPONSE WITH $10\mu F$ LOAD

Typical Performance Characteristics Curves ($V_{OUT} = 3.0V$) $V_{IN} = 5.0V, I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

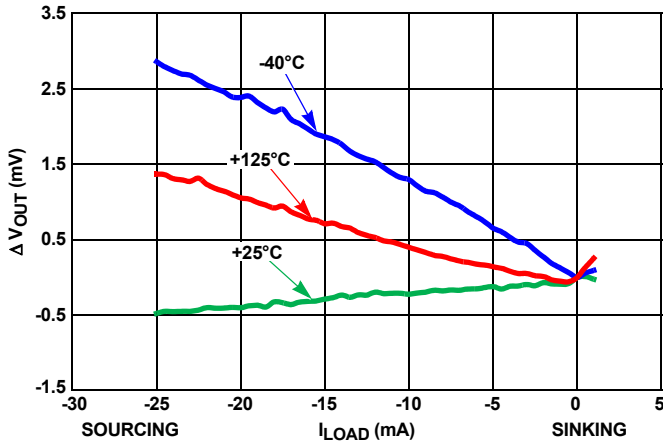


FIGURE 93. LOAD REGULATION OVER-TEMPERATURE

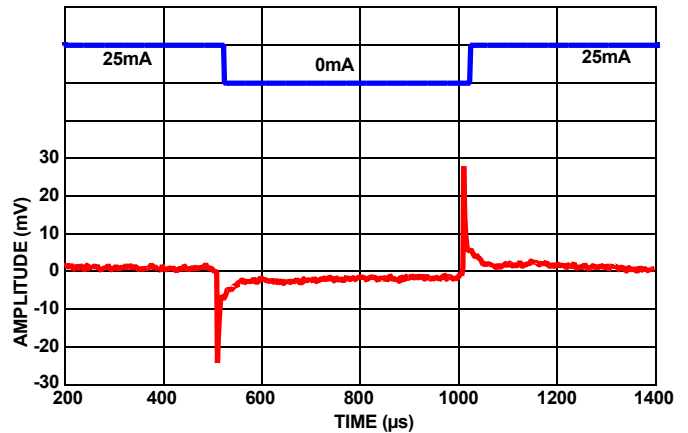


FIGURE 94. LOAD TRANSIENT RESPONSE AT 25mA LOAD AT 1µF

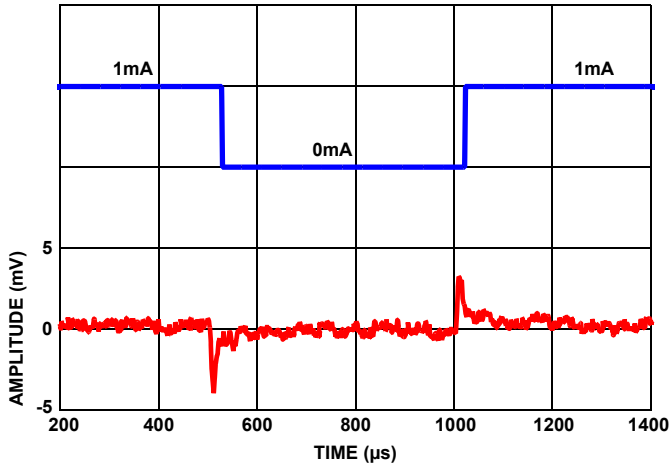


FIGURE 95. LOAD TRANSIENT RESPONSE AT 1mA LOAD AT 1µF

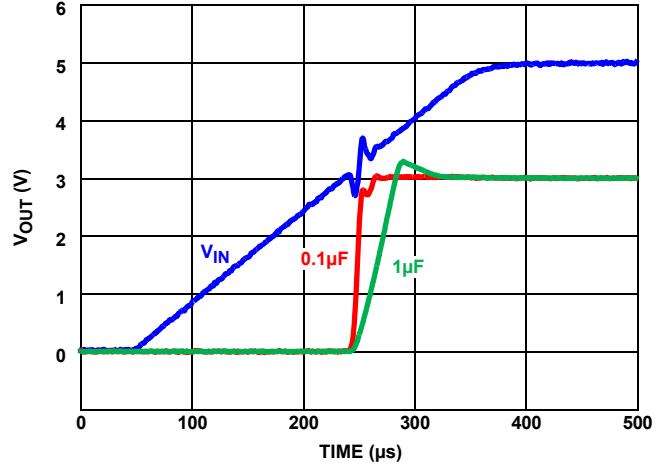


FIGURE 96. TURN-ON TIME

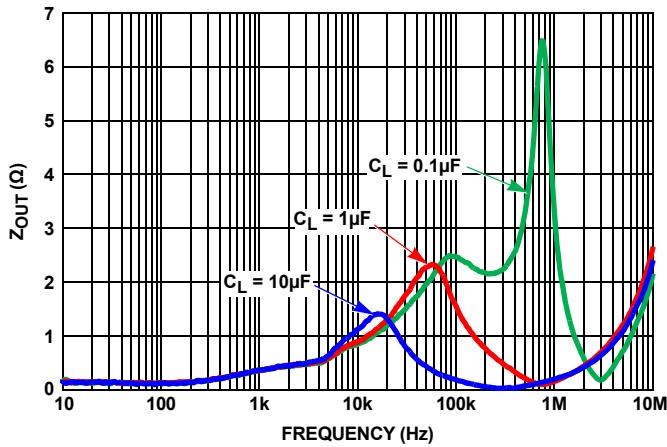


FIGURE 97. Z_{OUT} vs FREQUENCY

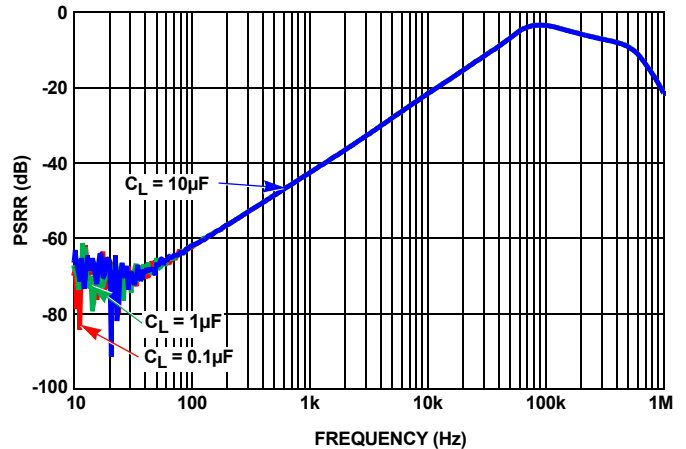


FIGURE 98. RIPPLE REJECTION AT DIFFERENT CAPACITIVE LOADS

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Typical Performance Characteristics Curves ($V_{OUT} = 3.0V$) $V_{IN} = 5.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified. (Continued)

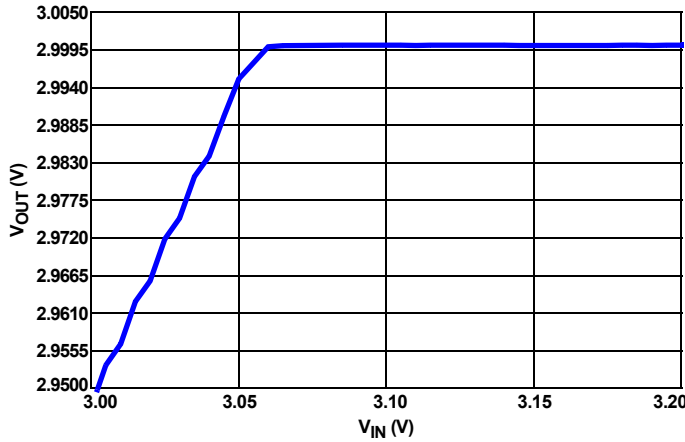


FIGURE 99. DROPOUT (10mA SOURCED LOAD)

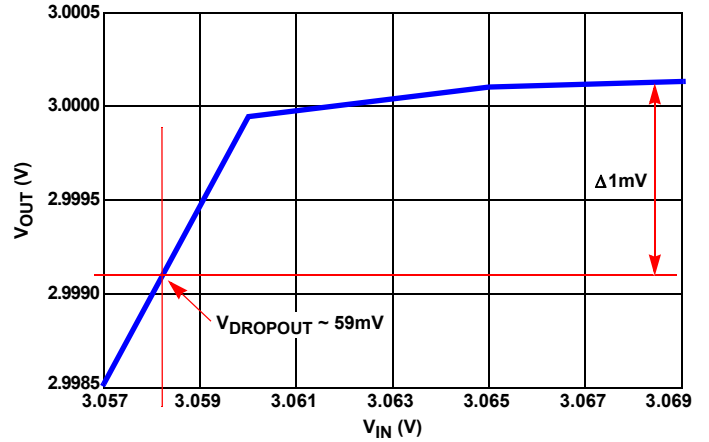


FIGURE 100. DROPOUT ZOOMED (10mA SOURCED LOAD)

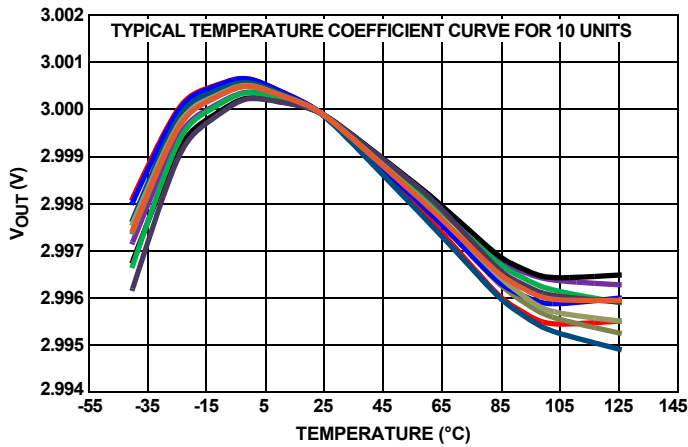


FIGURE 101. V_{OUT} vs TEMPERATURE

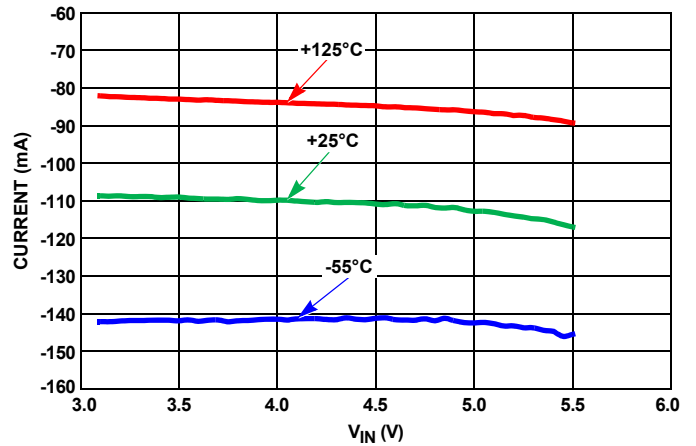


FIGURE 102. SHORT-CIRCUIT TO GND

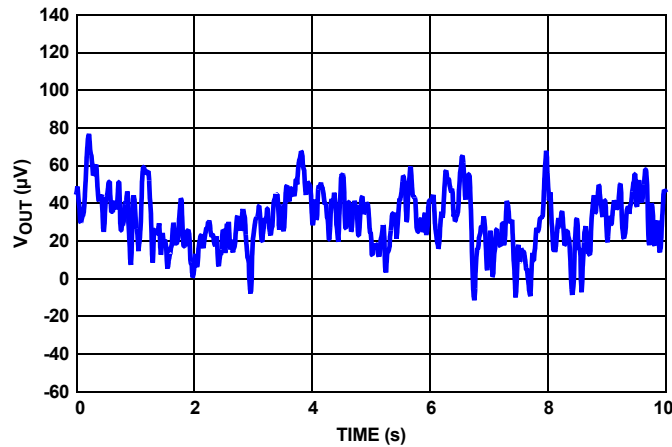


FIGURE 103. V_{OUT} vs NOISE, 0.1Hz TO 10Hz

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Typical Performance Characteristics Curves ($V_{OUT} = 3.3V$) $V_{IN} = 5.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified.

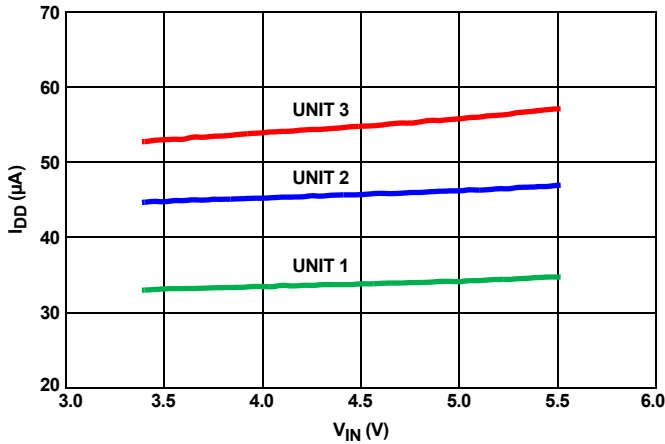


FIGURE 104. I_{IN} vs V_{IN} , THREE UNITS

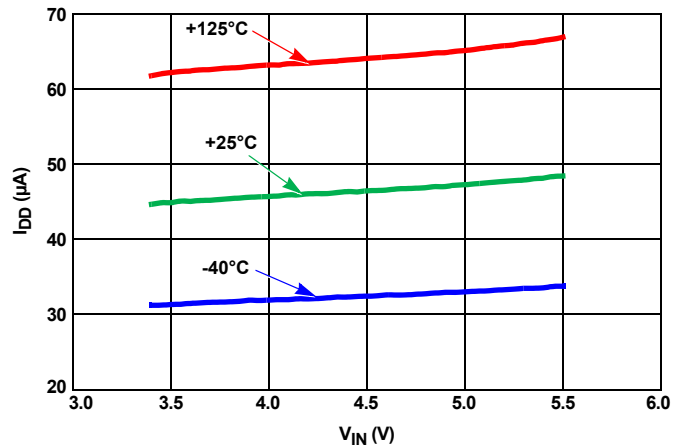


FIGURE 105. I_{IN} vs V_{IN} , OVER-TEMPERATURE

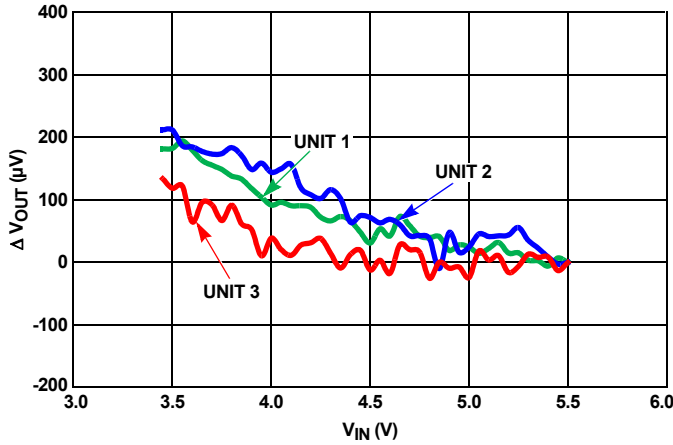


FIGURE 106. LINE REGULATION, THREE UNITS

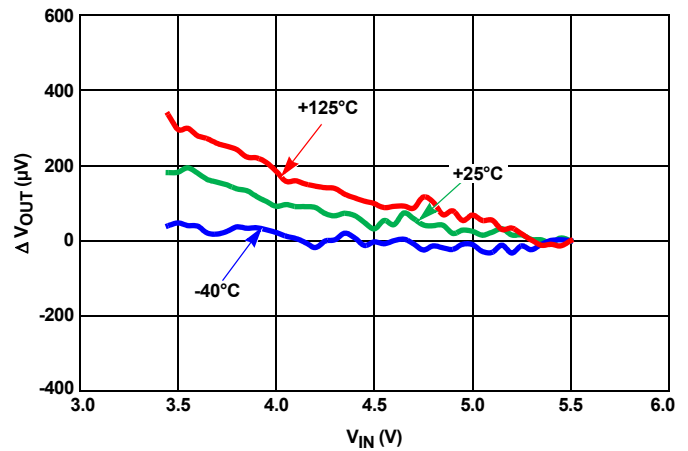


FIGURE 107. LINE REGULATION OVER-TEMPERATURE

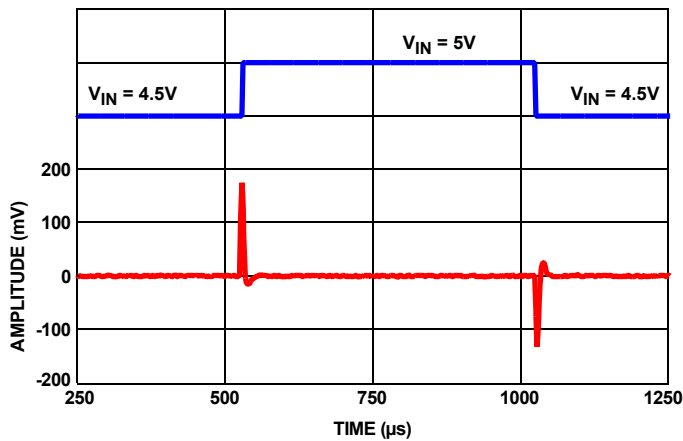


FIGURE 108. LINE TRANSIENT WITH $0.1\mu F$ LOAD

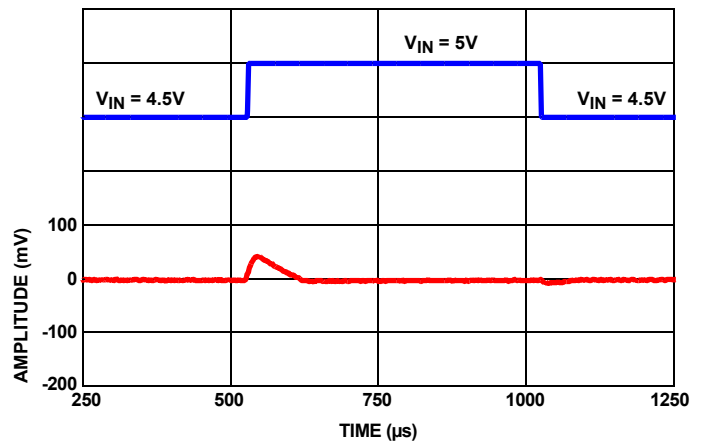


FIGURE 109. LINE TRANSIENT RESPONSE WITH $10\mu F$ LOAD

Typical Performance Characteristics Curves ($V_{OUT} = 3.3V$) $V_{IN} = 5.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified. (Continued)

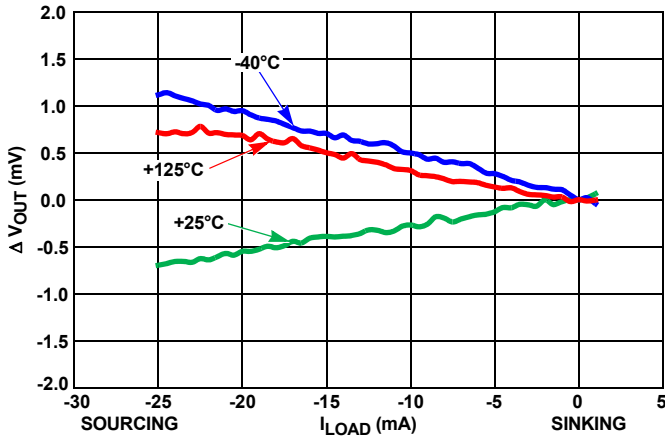


FIGURE 110. LOAD REGULATION OVER-TEMPERATURE

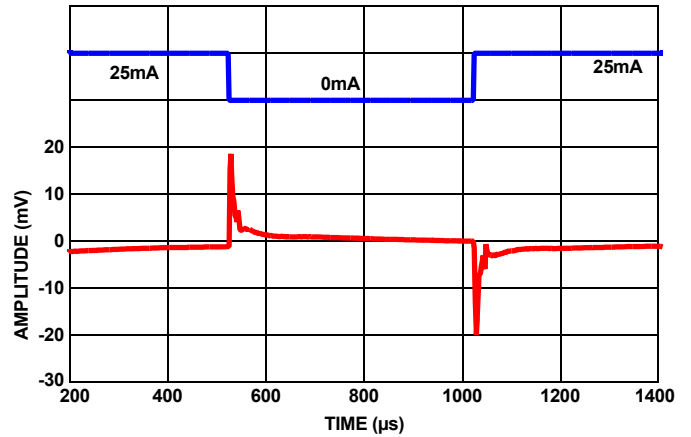


FIGURE 111. LOAD TRANSIENT RESPONSE AT 25mA LOAD AT 1µF

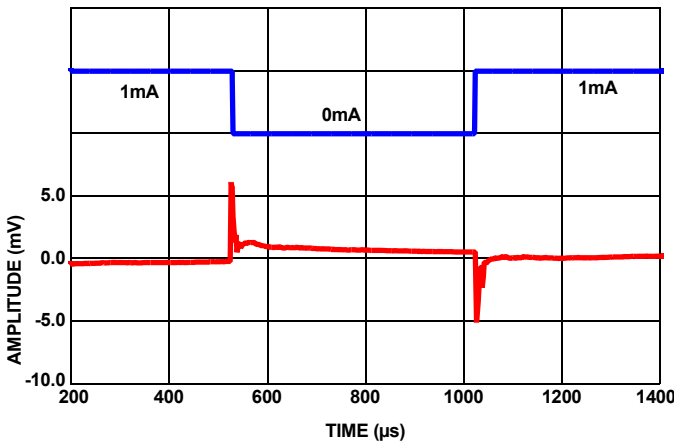


FIGURE 112. LOAD TRANSIENT RESPONSE AT 1mA LOAD AT 1µF

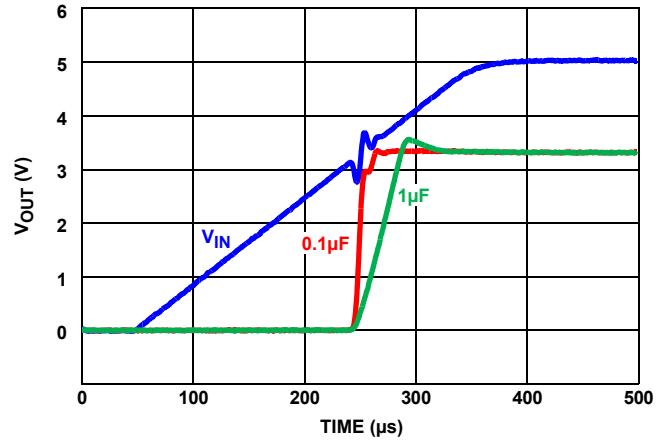


FIGURE 113. TURN-ON TIME

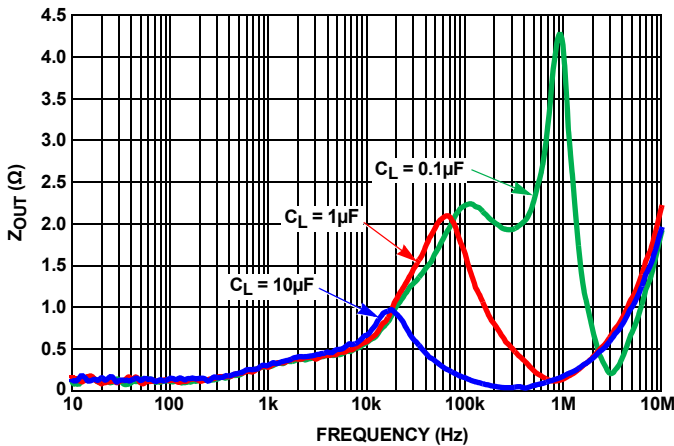


FIGURE 114. Z_{OUT} vs FREQUENCY

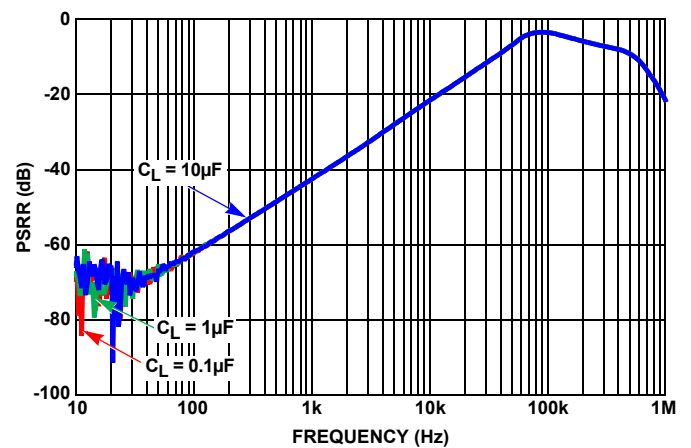


FIGURE 115. RIPPLE REJECTION AT DIFFERENT CAPACITIVE LOADS

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Typical Performance Characteristics Curves ($V_{OUT} = 3.3V$) $V_{IN} = 5.0V, I_{OUT} = 0mA,$ $T_A = +25^\circ C$ unless otherwise specified. (Continued)

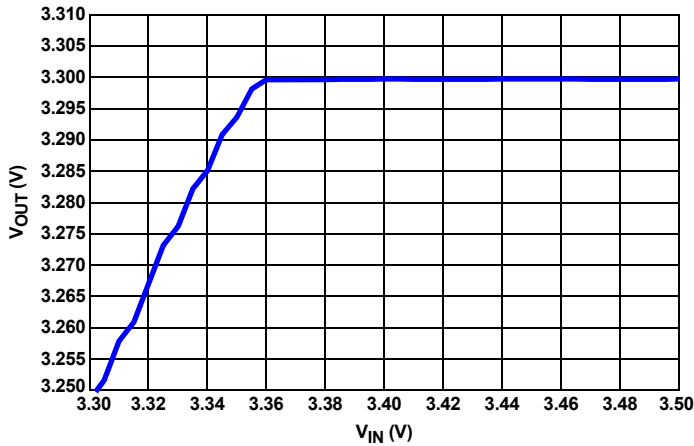


FIGURE 116. DROPOUT (10mA SOURCED LOAD)

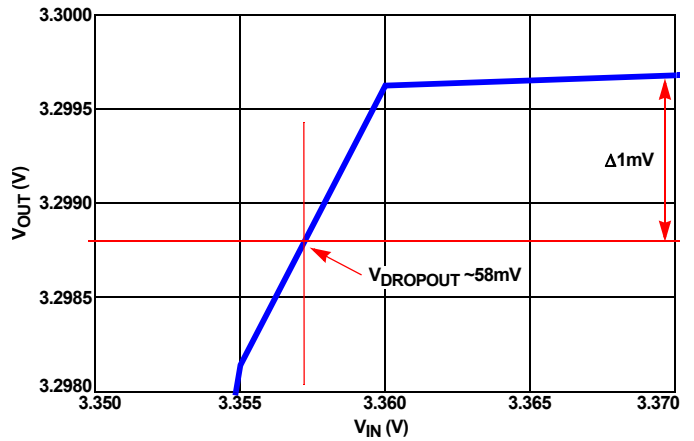


FIGURE 117. DROPOUT ZOOMED (10mA SOURCED LOAD)

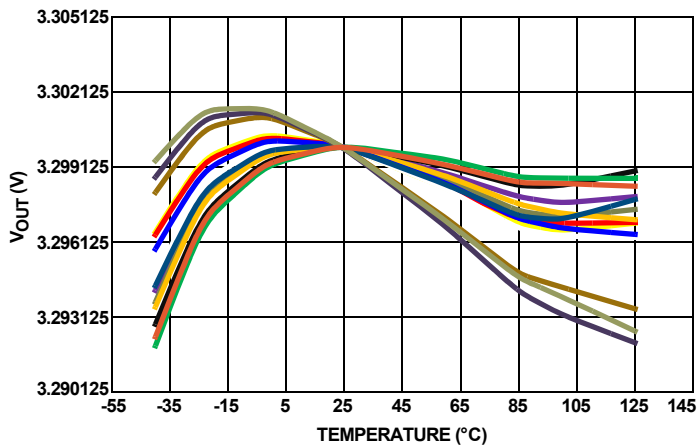


FIGURE 118. V_{OUT} vs TEMPERATURE

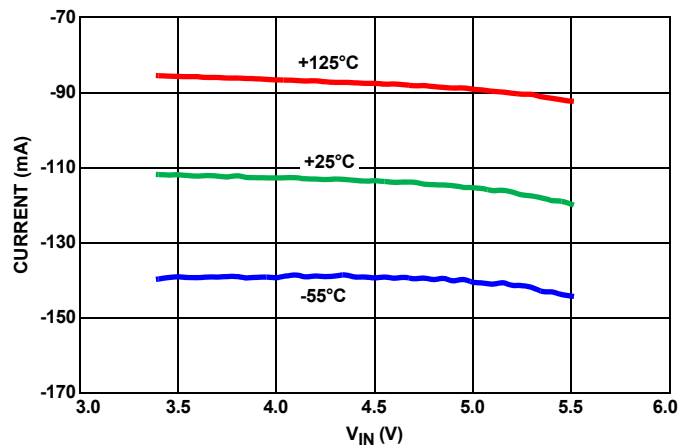


FIGURE 119. SHORT-CIRCUIT TO GND

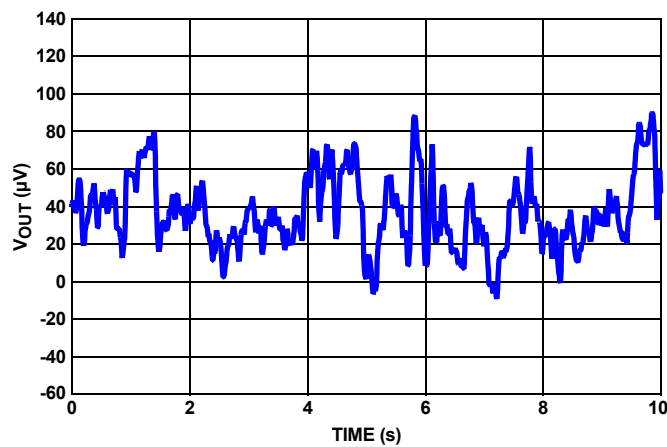


FIGURE 120. V_{OUT} vs NOISE, 0.1Hz TO 10Hz

Typical Performance Characteristics Curves ($V_{OUT} = 4.096V$) $V_{IN} = 3.0V$,

$I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified.

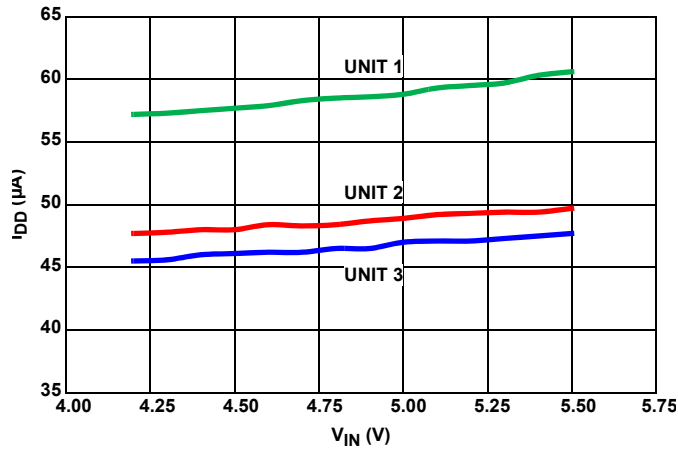


FIGURE 121. I_{IN} vs V_{IN} , THREE UNITS

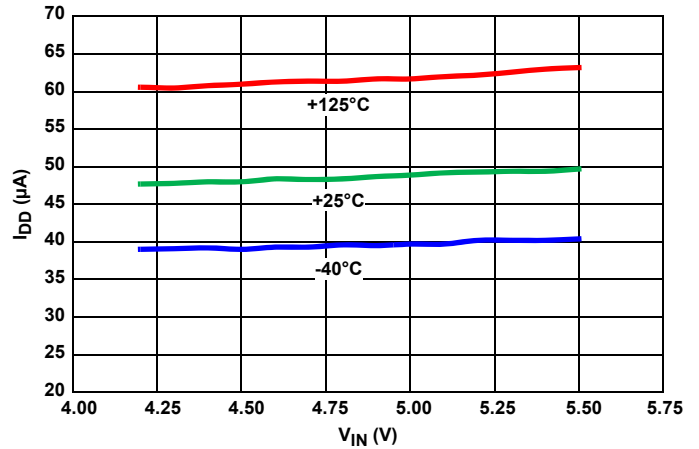


FIGURE 122. I_{IN} vs V_{IN} , OVER-TEMPERATURE

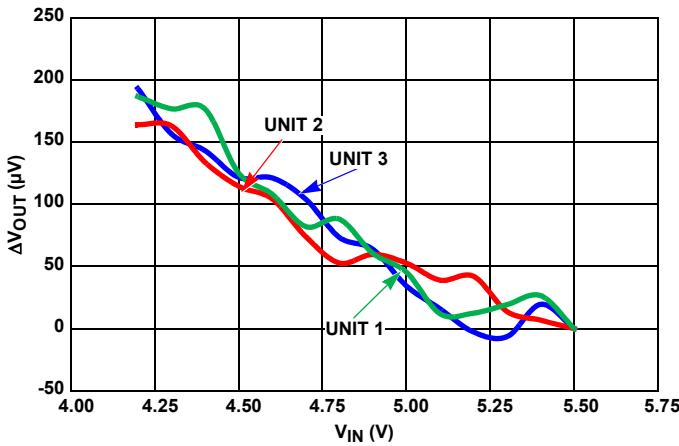


FIGURE 123. LINE REGULATION, THREE UNITS

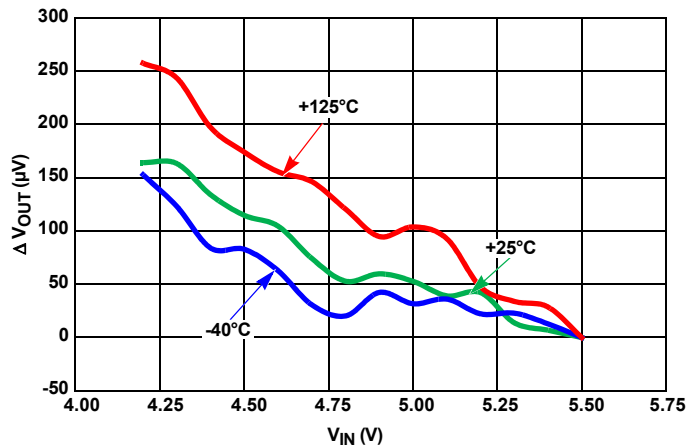


FIGURE 124. LINE REGULATION OVER-TEMPERATURE

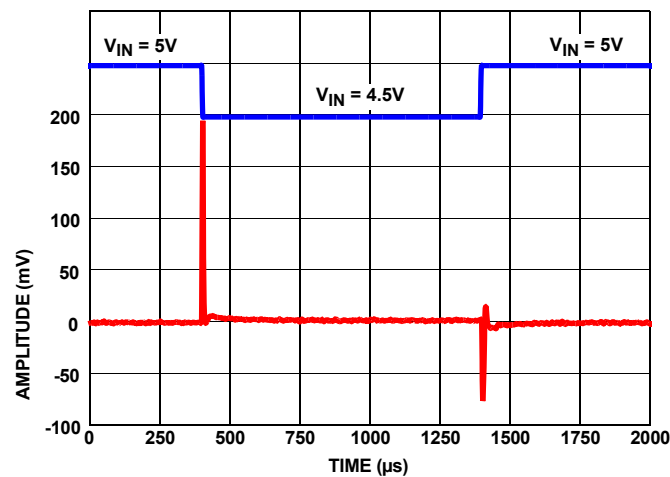


FIGURE 125. LINE TRANSIENT RESPONSE WITH $0.1\mu F$ LOAD

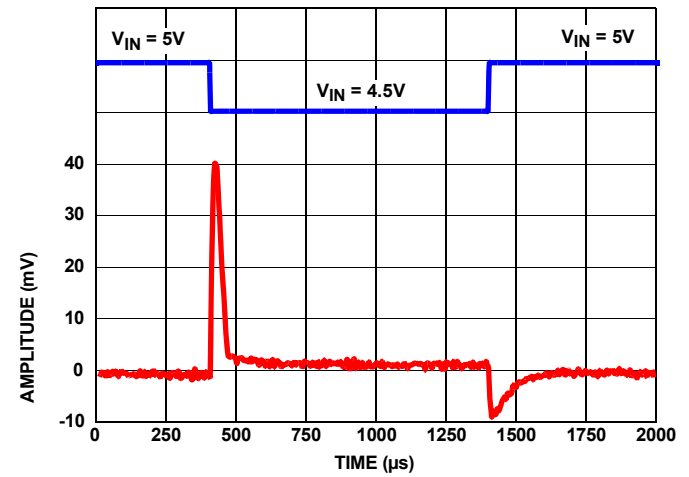


FIGURE 126. LINE TRANSIENT RESPONSE WITH $10\mu F$ LOAD

Typical Performance Characteristics Curves ($V_{OUT} = 4.096V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

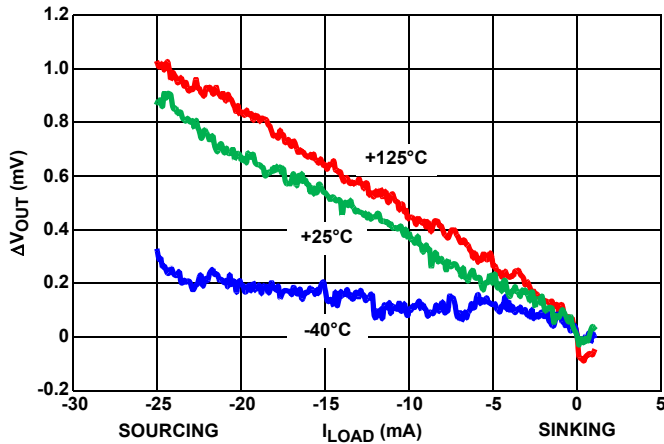


FIGURE 127. LOAD REGULATION OVER-TEMPERATURE

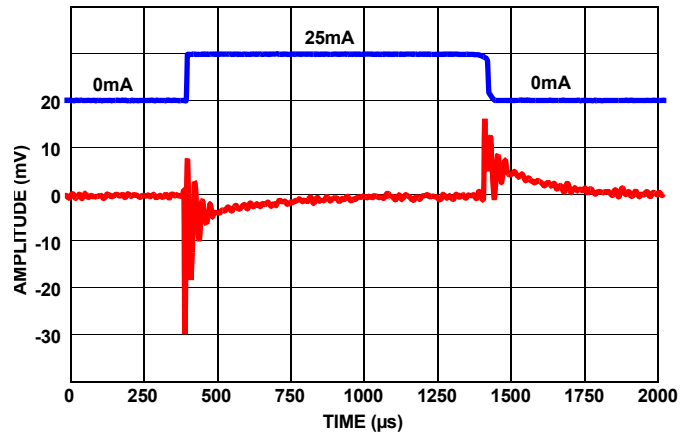


FIGURE 128. LOAD TRANSIENT RESPONSE AT 25mA LOAD AT 1μF

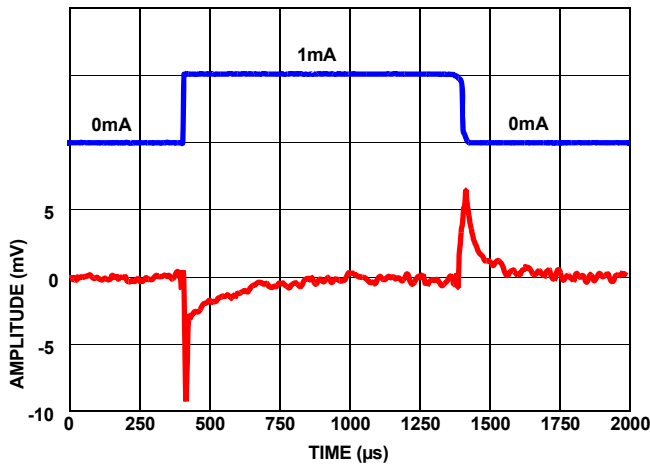


FIGURE 129. LOAD TRANSIENT RESPONSE AT 1mA LOAD AT 1μF

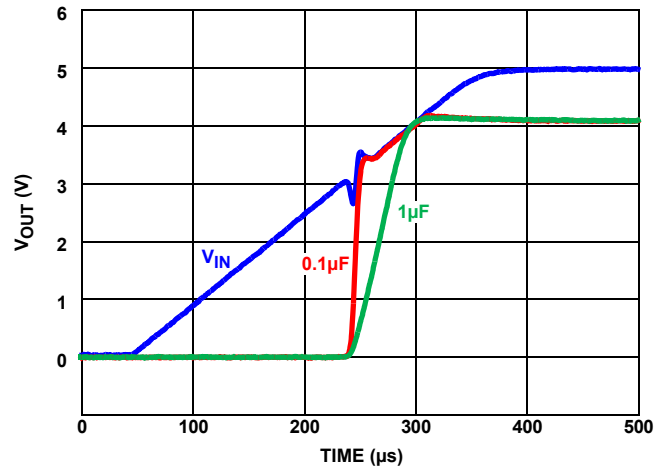


FIGURE 130. TURN-ON TIME

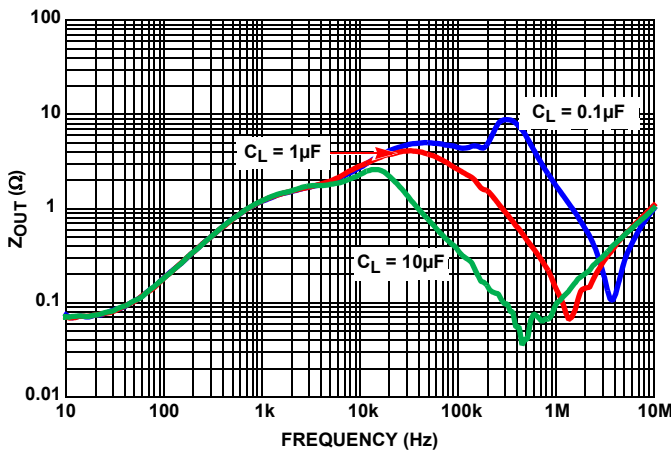


FIGURE 131. Z_{OUT} vs FREQUENCY

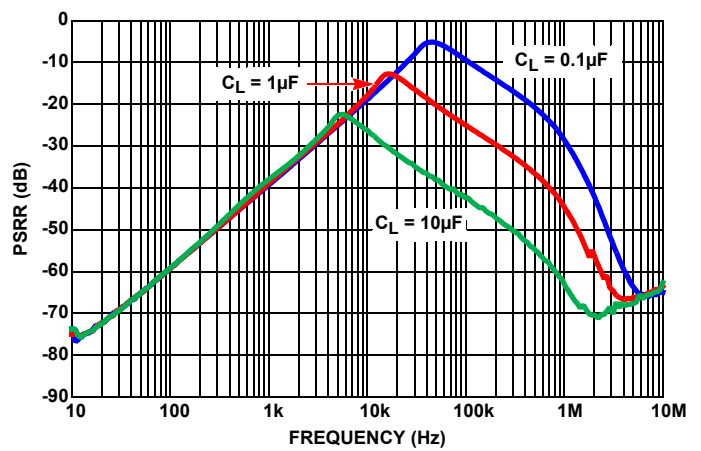


FIGURE 132. RIPPLE REJECTION AT DIFFERENT CAPACITIVE LOADS

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Typical Performance Characteristics Curves ($V_{OUT} = 4.096V$) $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

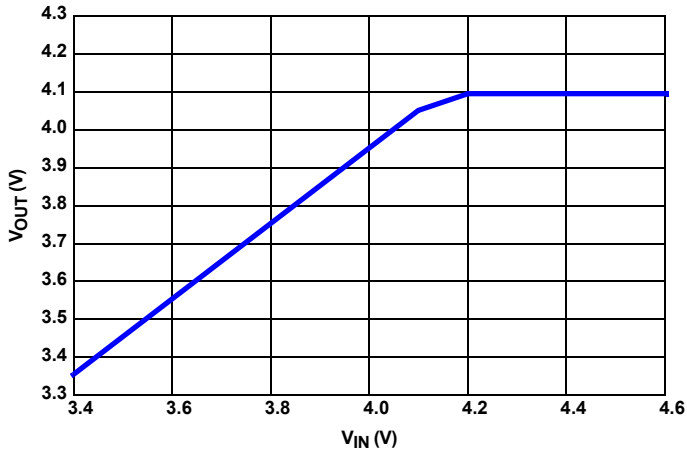


FIGURE 133. DROPOUT (10mA SOURCED LOAD)

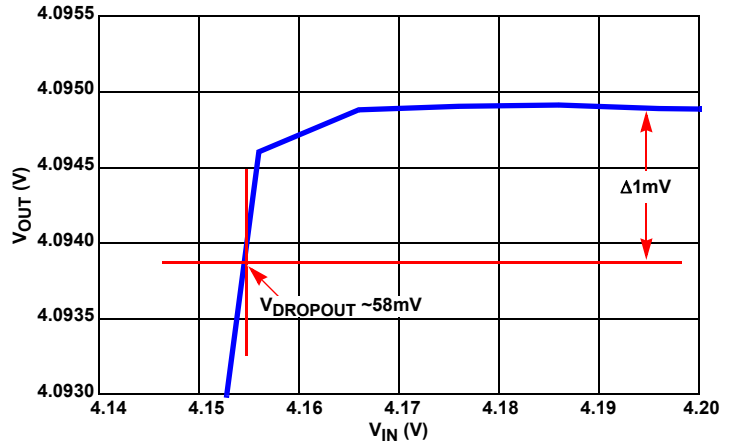


FIGURE 134. DROPOUT ZOOMED (10mA SOURCED LOAD)

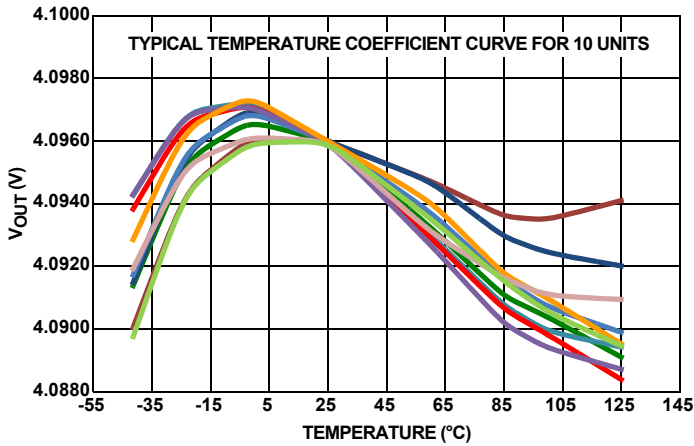


FIGURE 135. V_{OUT} vs TEMPERATURE

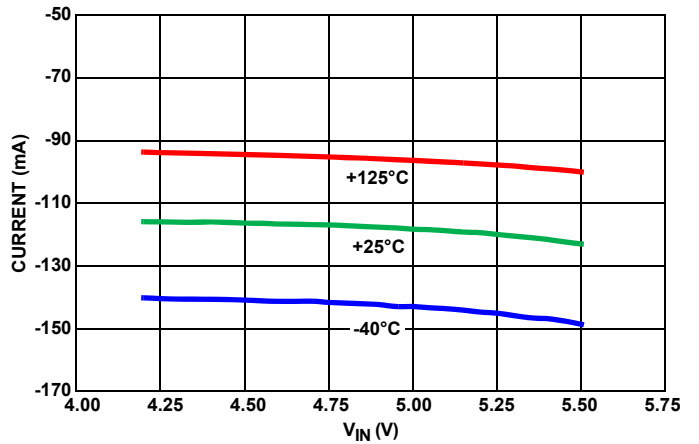


FIGURE 136. SHORT-CIRCUIT TO GND

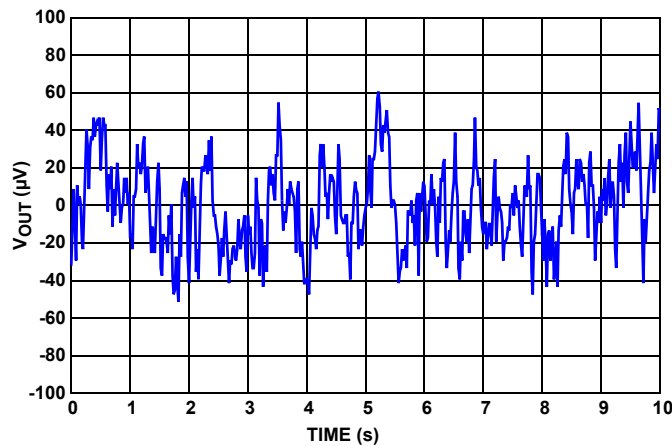


FIGURE 137. V_{OUT} vs NOISE, 0.1Hz TO 10Hz

Applications Information

Micropower Operation

The ISL21010 consumes very low supply current due to the proprietary bandgap technology. Low noise performance is achieved using optimized biasing techniques. Supply current is typically 48 μ A and noise in the 0.1Hz to 10Hz bandwidth is 58 μ V_{P-P} to 100 μ V_{P-P} (V_{OUT} = 2.048V, 3.0V and 3.3V) benefiting precision, low noise portable applications such as handheld meters and instruments.

Data Converters in particular can utilize the ISL21010 as an external voltage reference. Low power DAC and ADC circuits will realize maximum resolution with lowest noise. The device maintains output voltage during conversion cycles with fast response, although it is helpful to add an output capacitor, typically 1 μ F.

Board Mounting Considerations

For applications requiring the highest accuracy, board mounting location should be reviewed. The device uses a plastic SOIC package, which will subject the die to mild stresses when the Printed Circuit (PC) board is heated and cooled, slightly changing the shape. Placing the device in areas subject to slight twisting can cause degradation of the accuracy of the reference voltage

due to these die stresses. It is normally best to place the device near the edge of a board, or the shortest side, as the axis of bending is most limited at that location. Mounting the device in a cutout also minimizes flex. Obviously mounting the device on flexprint or extremely thin PC material will likewise cause loss of reference accuracy.

Board Assembly Considerations

Bandgap references provide high accuracy and low temperature drift but some PC board assembly precautions are necessary. Normal output voltage shifts of 100 μ V to 4mV can be expected with Pb-free reflow profiles or wave solder on multilayer FR4 PC boards. Precautions should be taken to avoid excessive heat or extended exposure to high reflow or wave solder temperatures, this may reduce device initial accuracy.

Noise Performance and Reduction

The recommended capacitive load range for the ISL21010 is from 0.1 μ F to 10.0 μ F (0.22 μ F minimum required for 1.024V option) to ensure stability and best transient performance. Parallel 0.1 μ F (0.22 μ F for 1.024V) and 10 μ F capacitors can be used to optimize performance as well. The noise specification stated in the Electrical Specification tables (starting on [page 4](#)) is for 0.1 μ F (0.22 μ F for 1.024V option) capacitive load, and larger values will reduce the output noise level.

Typical Application Circuit

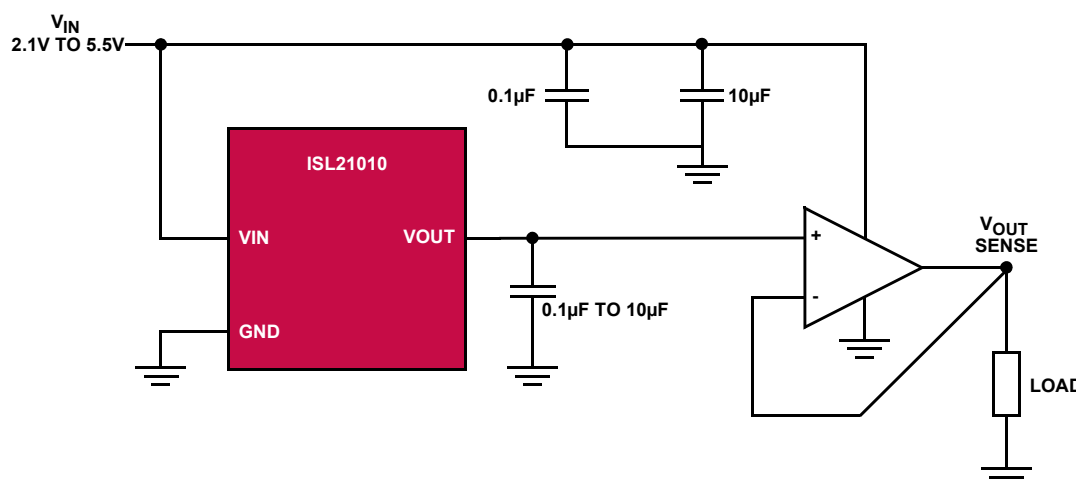


FIGURE 138. KELVIN SENSED LOAD

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Revision History

The revision history provided is for informational purposes only and is believed to be accurate, but not warranted. Please go to web to make sure you have the latest Rev.

DATE	REVISION	CHANGE
February 12, 2016	FN7896.4	<ul style="list-style-type: none"> Removed DAQ on a stick reference from "Related Literature" on page 1. Updated "Ordering Information" on page 3 by adding column for tape and reel option. Updated HBM value to kV (5500V to 5.5kV) in "Absolute Maximum Ratings" on page 4.
January 8, 2015	FN7896.3	<ul style="list-style-type: none"> On page 1, in the Related Literature section added AN1853 and AN1883. On page 3, updated the ordering information table by adding the (-T7A) products. Changed the y-axis units on Figure 18 on page 11 from "(V)" to "(μV)".
June 23, 2014	FN7896.2	<ul style="list-style-type: none"> Added Curves for Voltage Refs 1.25V, 1.024V, 1.5V, 2.5V and 4.096V Updated POD with following changes: In Detail A, changed lead width dimension from 0.13+/-0.05 to 0.085-0.19 Changed dimension of foot of lead from 0.31+/-0.10 to 0.38+/-0.10 In Land Pattern, added 0.4 Rad Typ dimension In Side View, changed height of package from 0.91+/-0.03 to 0.95+/-0.07
November 28, 2011	FN7896.1	<ul style="list-style-type: none"> On page 1, Features: removed "Coming Soon" from ISL21010-10, -12, -15; ISL21010-25; and ISL21010-40 voltage options; combined -20 option with -10, -12, -15; changed -40 to -41 On page 3, Ordering Information: added parts ISL21010DFH310Z-TK, ISL21010DFH312Z-TK, ISL21010CFH315Z-TK, ISL21010CFH325Z-TK, ISL21010CFH341Z-TK On page 4, Recommended Operating Conditions: added VOUT = 1.024V, 1.25V, 1.5V, 2.048V, 2.2V to 5.5V; VOUT = 2.5V.....2.6V to 5.5V; VOUT = 4.096V.....4.2V to 5.5V On page 4 through page 8, added Electrical Specifications tables for (ISL21010-10, VOUT = 1.024V), (ISL21010-12, VOUT = 1.25V), (ISL21010-15, VOUT = 1.5V), (ISL21010-41, VOUT = 4.096V) On page 6, Electrical Specifications (ISL21010-20, VOUT = 2.048V): changed VOUT/ TA, Thermal Hysteresis, TYP from 50 to 100 On page 8, Note 10: changed "... where V_{OUT} drops 1mV from V_{IN} = 5.0V at T_A = +25° C." to "... where V_{OUT} drops 1mV from V_{IN} = nominal at T_A = +25° C." On page 25, Figure 94, changed title from "LOAD REGULATION OVER-TEMPERATURE" to "LOAD TRANSIENT RESPONSE AT 25mA LOAD". Figure 27, changed title from "LOAD TRANSIENT RESPONSE" to "LOAD TRANSIENT RESPONSE AT 1mA LOAD". On page 26, Figure 99, and page 29, Figure 116, changed figure titles to indicate 10mA instead of 1mA source load. On page 28, Figure 111, changed title from "LOAD REGULATION OVER-TEMPERATURE" to "LOAD TRANSIENT RESPONSE AT 25mA LOAD". Figure 112, changed title from "LOAD TRANSIENT RESPONSE" to "LOAD TRANSIENT RESPONSE AT 1mA LOAD" On page 33, under "Noise Performance and Reduction", added reference to capacitive load range for 1.024V option.
August 9, 2011	FN7896.0	Initial Release

About Intersil

Intersil Corporation is a leading provider of innovative power management and precision analog solutions. The company's products address some of the largest markets within the industrial and infrastructure, mobile computing and high-end consumer markets.

For the most updated datasheet, application notes, related documentation and related parts, please see the respective product information page found at www.intersil.com.

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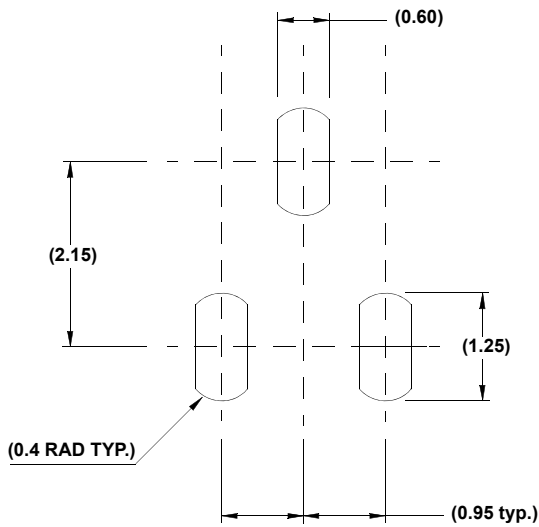
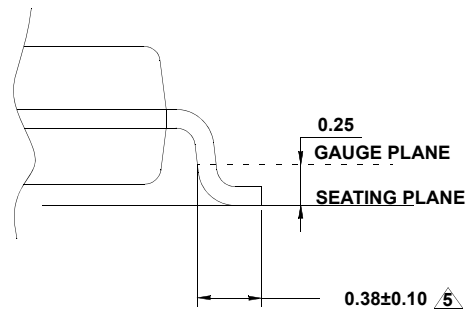
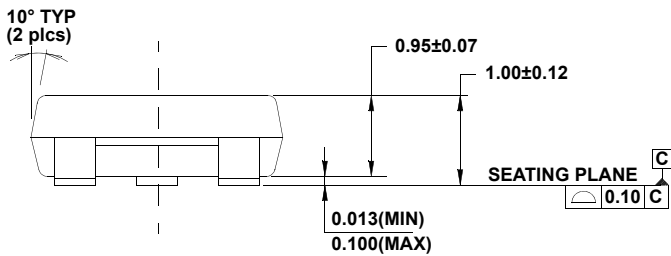
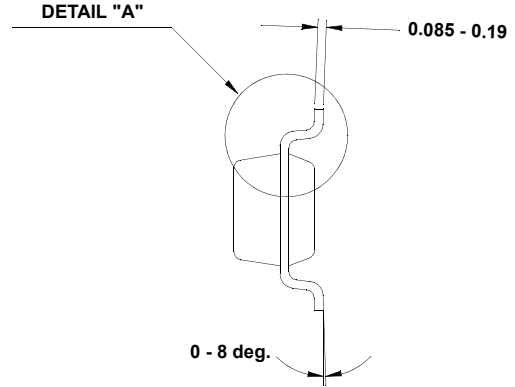
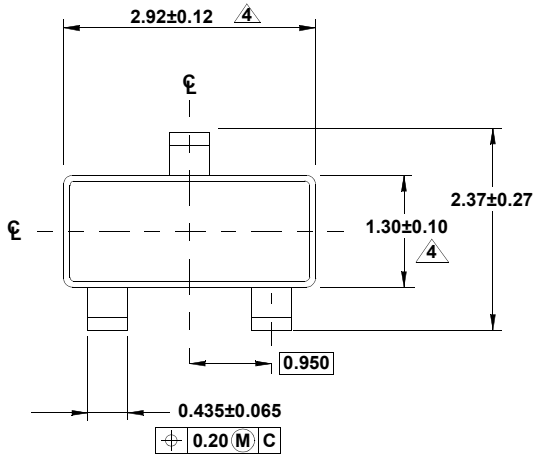
Reliability reports are also available from our website at www.intersil.com/support.

Package Outline Drawing

P3.064

3 LEAD SMALL OUTLINE TRANSISTOR PLASTIC PACKAGE (SOT23-3)

Rev 3, 3/12



NOTES:

1. Dimensions are in millimeters. Dimensions in () for Reference Only.
2. Dimensioning and tolerancing conform to AMSEY14.5m-1994.
3. Reference JEDEC TO-236.
4. Dimension does not include interlead flash or protrusions. Interlead flash or protrusions shall not exceed 0.25mm per side.
5. Footlength is measured at reference to gauge plane.